

# THAPAR UNIVERSITY, PATIALA - 147 004

(Established u/s 3 of the UGC act, 1956 vide Notification No. F-9-12/84-u.3 of GoI)

## **WALK IN INTERVIEW**

Walk in Interview for the following posts in the project “Special Manpower Development Program for VLSI and Related Software” sponsored by MoCIT (Govt. of India), New Delhi (on temporary basis) will be held on **July 16, 2010** at **9:30 am** in the **office of Head, ECE Department**.

**Lab Engineer – 1** (10,000/- to 20,000/- consolidated)

QUALIFICATIONS: BE/B.Tech. in Computer Science / Computer Engineering / Electronics or equivalent

DESIRABLE: ME/M.Tech. in VLSI Design / Microelectronics / Computer Science. Exposure to VLSI design tools.

JOB REQUIREMENT: Hardware / Software installations and maintenance, conduction of VLSI / embedded systems related classes to support the execution of VLSI Projects and related activities.

**Project Faculty – 1** (20,000/- to 24,000/- consolidated)

QUALIFICATIONS: ME/M.Tech. in VLSI Design / Microelectronics / or equivalent

JOB REQUIREMENT: Teaching VLSI / Embedded systems related courses to support the execution of VLSI Projects and related activities.

Interested candidates must bring with them original certificates, a copy of resume mentioning educational qualifications, DOB, professional experience (if any), contact details including email and phone numbers, etc. and a photocopy set of all documents. No TA/DA will be paid for attending the interview. An advance copy of resume may be forward to **the coordinator** at the address given below:

***Alpana Agarwal***

***Coordinator, SMDP-VLSI (Phase – II) Project  
ECED, Thapar University, PATIALA – 147004.***

***Email: [alpana@thapar.edu](mailto:alpana@thapar.edu) ; [alpana.tiet@gmail.com](mailto:alpana.tiet@gmail.com)***

***Phone: 0175-2393825 (o) 9417397370(m);***

***website: [www.thapar.edu](http://www.thapar.edu)***