THAPAR UNIVERSITY, PATIALA
ADVERTISEMENT

Walk-in Interview

DATE 03-03-2016

DEPARTMENT OF ELECTRONICS ENGINEERING

Interested candidates are requested to appear in the interview to be held on 30 March, 2016, at 10.00 AM in the ECED Department.

A) Applications are invited for the eligible candidates (Indian nationals only) on a plain paper for the temporary post in the research project entitled "Design and Development of load resilient mock-up ICRH system of Tokamak with variable load" which is subject interest of high power RF/Microwave Engineering, sponsored by the Board of Research in Nuclear Sciences (BRNS), Plasma and Fusion Research Committee (PFRC). The experimental work shall be carried out in the research labs of TU and in the collaborating industry. The candidate may be required to perform some experimental part at Institute for Plasma Research (IPR), Gandhinagar, an autonomous institute under Department of Atomic Energy (DAE).

<table>
<thead>
<tr>
<th>Name of the post</th>
<th>No. of posts</th>
<th>Stipend</th>
<th>Tenure of the post</th>
<th>Requirements for the post</th>
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<tr>
<td>Junior Research Fellow</td>
<td>01</td>
<td>INR. 25,000/-p.m.</td>
<td>2+1** Years</td>
<td>Essential: First class or equivalent in M.Tech. in Electronic Engineering/Electronics and Communication Engineering Age Limit: 28 as on November 01, 2015. **Note: On completion of two years and on basis assessment, Junior Research Fellow (JRF) shall be re-designated as Senior Research Fellow (SRF)</td>
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A. Applicants should note that the appointments are to be made are purely temporary and there is no right for claiming for any regular appointment in the University. The appointment would initially be for one year and is extendable based on satisfactory performance.

B. No TA/DA will be paid for attending the interview and/or at the time of joining the post.

C. The application should be submitted in the enclosed format which must be accompanied. The original certificates should be brought for verification at the time of interview.

D. JRF has option to enroll in Ph.D. at TU as per available guidelines under this project.

E. TU reserves the right to fix suitable criteria for short-listing the eligible candidates for the personal interview; short listed candidates will be called by e-mail only.

F. Address to which the application should be sent:

G. The candidates who supposed to complete their M.Tech. by May-2016 can also apply.

H. The candidates who already appeared in interview conducted on dated 18.01.16 need not to apply.

Dr. Rana Pratap Yadav, Electronics and communication Engineering Department, Thapar University, Patiala-147004, an advance copy of the application may be sent by

E-mail to: rpyadav@thapar.edu; ranayadav97@gmail.com

Signature of the Principal Investigator
FORMAT OF APPLICATION

(Attested copies should be enclosed in proof of all information furnished herein)

1) Application for the post of
2) Name of the candidate in full (in Capital letters)
3) Date of Birth
4) Nationality
5) Mailing Address
6) Permanent address

7) Educational qualifications

<table>
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<th>S.N.</th>
<th>Name of the Exam Passed</th>
<th>Subjects /Specializations</th>
<th>Name of the Institution Board/Univ.</th>
<th>Month &amp; Year of Exam passed</th>
<th>Marks secured</th>
<th>Percentage of Marks secured</th>
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8) Have you passed NET/CSIR-UGC joint Exam./GATE
   If 'yes', please indicate:
   (i) Name of the exam. Passed and score
   (ii) Date of exam. Passed

9) Previous experience:
   (Furnish details such as name of the post/fellowship;
   Period worked, name of the organization/institution etc.)

10) Present position:
    (a) If you are studying, please mention course/subject details.
    (b) Details of fellowship/Post
    (c) Name of the institution/organization in which you are studying/working.

11) Any other information

Date: ____________________________

Signature of the Candidate