COURSES SCHEME & SYLLABUS FOR M.E. ELECTRONICS & COMMUNICATION ENGINEERING 2015
COURSES SCHEME & SYLLABUS FOR M.E.
(ELECTRONICS AND COMMUNICATION ENGINEERING)

**SEMESTER – I**

<table>
<thead>
<tr>
<th>SR. NO.</th>
<th>COURSE NO.</th>
<th>TITLE</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>CR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PEC108</td>
<td>EMBEDDED SYSTEM DESIGN (FOUR SELF EFFORT HOURS FOR PROJECT - 2 CREDITS)</td>
<td>3</td>
<td>0</td>
<td>2+4</td>
<td>6.0</td>
</tr>
<tr>
<td>2</td>
<td>PWC104</td>
<td>STOCHASTIC PROCESSES AND INFORMATION THEORY</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>3.5</td>
</tr>
<tr>
<td>3</td>
<td>PEC101</td>
<td>DISCRETE TIME SIGNAL PROCESSING</td>
<td>3</td>
<td>1</td>
<td>2</td>
<td>4.5</td>
</tr>
<tr>
<td>4</td>
<td>PEC104</td>
<td>ANTENNA SYSTEMS</td>
<td>3</td>
<td>0</td>
<td>2</td>
<td>4.0</td>
</tr>
<tr>
<td>5</td>
<td>PEC105</td>
<td>ADVANCED COMMUNICATION SYSTEMS</td>
<td>3</td>
<td>1</td>
<td>2</td>
<td>4.5</td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td>15</td>
<td>3</td>
<td>12</td>
<td>22.5</td>
</tr>
</tbody>
</table>

**SEMESTER – II**

<table>
<thead>
<tr>
<th>SR. NO.</th>
<th>COURSE NO.</th>
<th>TITLE</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>CR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PWC203</td>
<td>ADVANCED WIRELESS COMMUNICATION SYSTEMS</td>
<td>3</td>
<td>1</td>
<td>2</td>
<td>4.5</td>
</tr>
<tr>
<td>2</td>
<td>PEC106</td>
<td>OPTICAL COMMUNICATION NETWORKS</td>
<td>3</td>
<td>0</td>
<td>2</td>
<td>4.0</td>
</tr>
<tr>
<td>3</td>
<td>PEC339</td>
<td>IMAGE PROCESSING AND COMPUTER VISION</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>2.0</td>
</tr>
<tr>
<td>4</td>
<td>PEC207</td>
<td>RF DEVICES AND APPLICATIONS</td>
<td>2</td>
<td>0</td>
<td>2</td>
<td>3.0</td>
</tr>
<tr>
<td>5</td>
<td>PEC291</td>
<td>SEMINAR</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>6</td>
<td>ELECTIVE – I</td>
<td></td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>3.0</td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td>11</td>
<td>1</td>
<td>6</td>
<td>19.5</td>
</tr>
</tbody>
</table>

**SEMESTER – III**

<table>
<thead>
<tr>
<th>SR. NO.</th>
<th>COURSE NO.</th>
<th>TITLE</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>CR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PEC392</td>
<td>MINOR PROJECT</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>4.0</td>
</tr>
<tr>
<td>2</td>
<td>PEC491</td>
<td>DISSERTATION (STARTS)</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>3</td>
<td></td>
<td>ELECTIVE – II</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>4</td>
<td></td>
<td>ELECTIVE – III</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td>6</td>
<td>0</td>
<td>0</td>
<td>10.0</td>
</tr>
</tbody>
</table>

**SEMESTER – IV**

<table>
<thead>
<tr>
<th>SR. NO.</th>
<th>COURSE NO.</th>
<th>TITLE</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>CR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PEC491</td>
<td>DISSERTATION (CONTD …)</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>12.0</td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>12.0</td>
</tr>
</tbody>
</table>

TOTAL CREDITS: 64.0
### ELECTIVE–I

<table>
<thead>
<tr>
<th>SR. NO.</th>
<th>COURSE NO.</th>
<th>TITLE</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>CR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PVL203</td>
<td>VLSI SIGNAL PROCESSING</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>2</td>
<td>PVL213</td>
<td>NANOELECTRONICS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>3</td>
<td>PEC327</td>
<td>PHOTONIC INTEGRATED DEVICES &amp; CIRCUITS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>4</td>
<td>PWC201</td>
<td>SPACE-TIME WIRELESS COMMUNICATION</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>5</td>
<td>PWC212</td>
<td>WIRELESS SECURITY</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>6</td>
<td>PWC202</td>
<td>ADVANCED WIRELESS NETWORKS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>7</td>
<td>PEC211</td>
<td>PASSIVE OPTICAL NETWORKS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>8</td>
<td>PEC212</td>
<td>AUDIO AND SPEECH PROCESSING</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>9</td>
<td>PEC215</td>
<td>DETECTION AND ESTIMATION THEORY</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>10</td>
<td>PEC216</td>
<td>ADVANCED COMPUTER NETWORKS AND PROTOCOLS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>11</td>
<td>PEC218</td>
<td>DIGITAL SIGNAL PROCESSORS</td>
<td>2</td>
<td>0</td>
<td>2</td>
<td>3.0</td>
</tr>
<tr>
<td>12</td>
<td>PEC219</td>
<td>MULTIMEDIA COMPRESSION TECHNIQUES</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>13</td>
<td>PEC214</td>
<td>FRACTIONAL TRANSFORMS AND APPLICATIONS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>14</td>
<td>PEC326</td>
<td>OPTOELECTRONICS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>15</td>
<td>PEC222</td>
<td>HDL AND SYSTEM C PROGRAMMING</td>
<td>2</td>
<td>0</td>
<td>2</td>
<td>3.0</td>
</tr>
</tbody>
</table>

### ELECTIVE–II

<table>
<thead>
<tr>
<th>SR. NO.</th>
<th>COURSE NO.</th>
<th>TITLE</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>CR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PVL334</td>
<td>HIGH SPEED VLSI DESIGN</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>2</td>
<td>PVL332</td>
<td>MIXED SIGNAL CIRCUIT DESIGN</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>3</td>
<td>PVL343</td>
<td>SENSOR TECHNOLOGY AND MEMS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>4</td>
<td>PVL211</td>
<td>VLSI ARCHITECTURE</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>5</td>
<td>PWC321</td>
<td>NEXT GENERATION WIRELESS SYSTEMS AND NETWORKS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>6</td>
<td>PWC322</td>
<td>ADVANCED ERROR CONTROL CODING THEORY</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>7</td>
<td>PWC323</td>
<td>WIRELESS BROADBAND NETWORKS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>8</td>
<td>PEC217</td>
<td>MICROSTRIP ANTENNAS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>9</td>
<td>PEC206</td>
<td>MACHINE LEARNING</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>10</td>
<td>PEC337</td>
<td>ADAPTIVE SIGNAL PROCESSING</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>11</td>
<td>PEC338</td>
<td>ROBOTICS AND AUTOMATION</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>12</td>
<td>PEC321</td>
<td>ADVANCED OPTICAL TECHNOLOGIES</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
</tbody>
</table>
### ELECTIVE-III

<table>
<thead>
<tr>
<th>SR. NO.</th>
<th>COURSE NO.</th>
<th>TITLE</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>CR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PVL333</td>
<td>SYSTEM ON CHIP</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>2</td>
<td>PVL346</td>
<td>ADVANCED ANALOG CIRCUIT DESIGN TECHNIQUES</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>3</td>
<td>PVL344</td>
<td>HARDWARE ALGORITHMS FOR COMPUTER ARITHMETIC</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>4</td>
<td>PWC335</td>
<td>WIRELESS SENSOR NETWORKS</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>5</td>
<td>PWC336</td>
<td>WIRELESS COMMUNICATION PROTOCOL</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>6</td>
<td>PWC337</td>
<td>SPREAD SPECTRUM COMMUNICATION</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>7</td>
<td>PEC301</td>
<td>ARTIFICIAL INTELLIGENCE</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>8</td>
<td>PEC303</td>
<td>BIOMEDICAL SIGNAL PROCESSING</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>9</td>
<td>PEC304</td>
<td>CLOUD COMPUTING</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>10</td>
<td>PEC205</td>
<td>RF CIRCUIT DESIGN</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>11</td>
<td>PEC331</td>
<td>IP OVER WDM</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
<tr>
<td>12</td>
<td>PVL347</td>
<td>SOFT COMPUTING TECHNIQUES</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3.0</td>
</tr>
</tbody>
</table>
PEC108: EMBEDDED SYSTEMS DESIGN

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3  0  6  6.0

Course Objective: To understand the basic concepts of embedded system, understanding of different types of programming languages used for embedded systems. Study of ARM based processors: architecture, programming and interfacing of ARM processor with memory & I/O devices. To discuss the features, Architecture and programming of Arduino Microcontroller, Architecture of Arduino. To study of RTOS.

Introduction to Embedded Systems: Background and History of embedded systems, Definition and Classification, Programming languages for embedded systems: Desirable characteristics of programming languages for embedded systems, Low-level versus high-level languages, Main language implementation issues: control, typing. Major programming languages for embedded systems. Embedded Systems on a Chip (SoC) and the use of VLSI designed circuits.

ARM Processor Fundamentals: ARM core data flow model, Architecture, ARM General purpose Register set and GPIO’s, CPSR, Pipeline, Exceptions, Interrupts, Vector Table, ARM processors family, ARM instruction set and Thumb Instruction set. ARM programming in Assembly, in C and C++ Instruction Scheduling, Conditional Execution, Looping Constructs, Bit Manipulation, Exception and Interrupt Handling.


Laboratory Work:
Introduction to ARM processor kit, Programming examples of ARM processor. Interfacing of LED, seven segment display, ADC and DAC with ARM processor. Raspberry Pi based projects.

Minor Project: ARM processor/Arduino Microcontroller/Raspberry Pi based project to be allocated to each student by the course instructor. (Four Self Effort Hours for Project – 2 Credits)

Course Learning Outcomes(CLOs):
1. Understanding of Embedded system, programming, Embedded Systems on a Chip (SoC) and the use of VLSI designed circuits.
2. Understanding of internal Architecture and programming of ARM processor.
4. Need of Real time Operating System (RTOS) in embedded systems.
5. Study of Real time Operating system with Task scheduling and Kernel Objectives.
Text Books:

Reference Books:

Evaluation Scheme:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Evaluation Elements</th>
<th>Weightage (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>MST</td>
<td>24%</td>
</tr>
<tr>
<td>2.</td>
<td>EST</td>
<td>36%</td>
</tr>
<tr>
<td>3.</td>
<td>Sessionals (May include Assignments/Projects/Tutorials/Quizes/Lab Evaluations)</td>
<td>40%</td>
</tr>
</tbody>
</table>
PWC104: STOCHASTIC PROCESSES AND INFORMATION THEORY

<table>
<thead>
<tr>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Cr.</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>1</td>
<td>0</td>
<td>3.5</td>
</tr>
</tbody>
</table>

**Course Objectives:** To gain and understand the complete knowledge of probability theory, random variables, stochastic processes, Information theory and source coding. To familiarize the students with the applications of probabilistic and stochastic methods in communication engineering and information theory problems.

**Probability and Induction:** Axioms of Probability, Set Theory, Probability Space, Conditional Probability, Repeated Trials, Combined Experiments, Bernoulli Trials, Bernoulli’s Theorem, and Games of Chance, Concept of a Random Variables, Distribution and Density, Function Specific Random Variables, Conditional Distributions, Binomial Random Variables, Functions of One Random Variable, its Distribution, Mean and Variance, Moments, Characteristic Functions; Bivariate Distributions, Two Functions of Two Random Variables, Joint Moments, Joint Characteristic Functions, Conditional Distributions, Conditional Expected Values, Normality, Stochastic Convergence and Center Limit Theorem

**Estimation & Hypothesis Testing:** Time and Ensemble Averages, Covariance and Correction Functions. Simple binary hypothesis tests, Decision Criteria, Neyman pearson tests, Bayes Criteria, Multiple Hypothesis testing, Composite hypothesis testing, Asymptotic Error rate of LRT for simple hypothesis testing


**Queueing Systems:** Characteristics of Queueing Process, birth-death process, arrival and service, steady state solution; M/G/1 and G/M/1, occupancy distribution, renewal theory, waiting time and busy period, Series Queues, Jackson Networks, Cyclic Queues. Little's theorem, modeling & analysis of M/M/- queues, Burke's Theorem, Reversibility, Queues with vacations, Work conservation principle, Priority queues, Queues served in cyclic order, Fluid-flow and diffusion approximations

**Statistical Modeling of Noise:** Probability density of a jointly-Gaussian random vector, Fourier transforms for joint densities, Wide-sense stationary (WSS) processes, Poisson process noise, sources of noise in communication systems, shot noise, resistor noise, calculation of noise in linear systems, noise bandwidth, noise temperature, noise in two-port networks, noise-figure, cascaded stages, signal in presence of AWGN, narrow band noise and colored noise
Information Theory: Unit of information, rate of information, joint entropy and conditional entropy, mutual information, Shannon-Hartley Theorem, bandwidth SNR trade off, channel capacity calculations of different channels, Source Coding- Coding efficiency, Shannon-Fano coding, Huffman coding, Lempel-Ziv adaptive coding

Course Learning Outcomes (CLO):
At the end of the course, the students should be able to:
1. Identify and formulate fundamental probability distribution and density functions, as well as, functions of random variables.
2. Explain the concepts of expectation and conditional expectation, and describe their properties.
3. Understand and analyze continuous and discrete time random processes.
4. Explain the concepts of stationarity and wide–sense–stationarity and appreciate their physical significance.
5. Employ the theory of stochastic processes to analyze linear systems, and to have knowledge about cross– and auto–correlation of stochastic processes.
6. To have deep knowledge about power spectral analysis of stationary stochastic processes and ergodicity.
7. Understand the basic concepts of information theory, Entropy and mutual information, models of communication channels, the ultimate limits of data transmission, the process of data compression.

Text Books:
2. John N. Daigle, Queueing theory with applications to packet telecommunication Springer (2005)

Reference Books:
5. IEEE Transactions on Information Theory

Evaluation Scheme:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Evaluation Elements</th>
<th>Weightage (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>MST</td>
<td>30</td>
</tr>
<tr>
<td>2.</td>
<td>EST</td>
<td>45</td>
</tr>
<tr>
<td>3.</td>
<td>Sessionals (May include Assignments/Projects/Tutorials/Quizes/Lab Evaluations)</td>
<td>25</td>
</tr>
</tbody>
</table>
PEC101: DISCRETE TIME SIGNAL PROCESSING

Course Objective: To introduce fundamentals of discrete-time linear systems and digital signal processing. Emphasizes theory but also includes design and applications.

Review of Discrete-Time (DT) signals and systems, Sampling and Reconstruction of signals, Z-transform, discrete-time Fourier transform (DTFT) and discrete Fourier transform (DFT), Divide and Conquer approach, The fast Fourier transform (FFT) algorithms: Decimation-in-Time and Decimation-in-Frequency FFT Algorithms.


Multi-Rate Signal Processing: Decimation and Interpolation by integer and rational factor, Aliasing error, Sample rate conversion, Poly-phase structures, Multistage implementation of Sampling rate converters, Multi-rate filter banks, Quadrature mirror filters, Applications.

Linear Prediction: Random signals, correlation function and power spectra, Forward & backward linear prediction, Solution to normal equations - Levinson-Durbin Algorithm, Schurz algorithm, Wiener filters for filtering.


Time-Frequency Analysis: Concept of time-frequency analysis, Forward and Inverse Wavelet transform, Wavelet families, Multi-resolution analysis.

Laboratory Work: Digital filter structures, Multi-rate signal processing, Prediction, Adaptive filters, Time-frequency analysis.

Minor Project: To be assigned by concerned instructor/course-coordinator
Course Learning Outcomes (CLOs):
The student will be able to learn
1. Discrete time signal processing and filter design techniques.
2. Multi-rate signal processing and its application.
3. Theory of adaptive filter design and its applications.
4. To estimate the spectra of random signals and variety of modern and classical spectrum estimation techniques.

Text Books:

Reference Books:

Evaluation Scheme:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Evaluation Elements</th>
<th>Weightage (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>MST</td>
<td>24%</td>
</tr>
<tr>
<td>2.</td>
<td>EST</td>
<td>36%</td>
</tr>
<tr>
<td>3.</td>
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PEC104: ANTENNA SYSTEMS

Course Objective: The goals of this course are to develop student’s analytical and intuitive understandings of antenna physics, and to introduce students to a large variety of antenna structures of practical interest related to recent developments in wireless communication and systems.


Thin Linear Antennas and Arrays: Short Electric dipole, Thin linear antenna, Radiation resistance of antennas, Radiation resistance at a point which is not a current maximum, Fields of a thin linear antenna with a uniform travelling wave, Array parameters, Half-power beam-width Mathematics of linear array, Antenna element spacing without grating lobes, Linear broadside array with non uniform distributions, Gain of regularly spaced planar arrays with $d = \lambda/2$, Tchebyscheff Array antennas, Reduction of side-lobes by tapering, Circular array, Phase and amplitude errors.

Secondary Sources and Aperture Antennas: Magnetic currents, Duality, Images of electric and magnetic currents, electric and magnetic currents as sheet sources, Impressed and induced current sources, Induction and equivalence theorems, Field of a secondary or Huygens source, Radiation from open end of a coaxial line, Radiation through an aperture in conducting screen, slot antenna.


Applications and Numerical Techniques: Different types of antennas for applications in communication systems, Antennas for space communication, Numerical techniques in antenna design.

Laboratory Work:
Practicals related to Antenna Techniques using Software and Hardware.

Minor Project: To be assigned by concerned instructor/course-coordinator.

Course Learning Outcomes (CLOs):
Upon completion of the course the student will
1. Acquire knowledge about basic antenna concepts.
2. Have ability to understand thin linear antennas and arrays.
3. Foster ability to understand secondary sources, aperture, broadband and frequency independent antennas.
4. Acquire knowledge about effect of mutual coupling on antennas, applications and numerical techniques.
5. Have ability to understand adaptive array concept.

Text Books:

Reference Books

Evaluation Scheme:

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PEC105: ADVANCED COMMUNICATION SYSTEMS  

L  T  P  Cr  
3  1  2  4.5

Course Objective: To introduce to analog and digital communication systems, Study of analog communication receivers, Study of signal designing for band limited channels, To introduce satellite communication and selected areas in communication.

Introduction: Introduction to analog and digital communication systems, Baseband, Band-pass and equivalent low pass signal representations, Concept of pre-envelope and Hilbert transform, Representation of band pass stochastic processes, Mathematics model of communication system.

Digital Pass Band Transmission and Optimum Receiver Design: Pass band transmission model, Gram Schmidt orthogonalization procedure, Geometric interpretation of signals, Response of bank of correlators to noisy input, Correlation demodulator, Matched filter demodulator, Optimum detector, Maximum likelihood sequence detector, A symbol by symbol MAP detector for signals, Probability of error calculations for band limited signal (ASK, PSK, QAM), Probability of error calculations for power limited signal (ASK, PSK, QAM), Probability of error calculations for binary modulation, M-ary orthogonal signals, bi-orthogonal signals, simplex signals.

Carrier and Symbol Synchronization: Likelihood function, carrier recovery and symbol synchronization in signal demodulation, ML carrier phase estimation, PLL, decision directed loops and non-decision directed loops, ML timing estimation, non-decision directed timing estimation, joint estimation of carrier phase and symbol timing.

Signal Design for Band Limited Channels: Characterization of band limited channels, design of band limited signals for no ISI, Design of band limited signals with controlled ISI, data detection for controlled ISI, signal design for channels with distortion, probability of error for detection of PAM with zero ISI and with partial response signals, modulation codes for spectrum shaping.

Communication through Band Limited Linear Filter Channels: ML receiver for channels with ISI and AWGN, discrete time model for channel with ISI, Viterbi algorithm for discrete time white noise filter model, Performance of MLSE for channels with ISI, linear equalization: peak distortion criterion, MSE criterion and its performance, fractionally spaced equalizers, decision feedback equalization: coefficient optimization, performance characteristics.

Laboratory Work:

Signal generation, modulation techniques, Equalizers carrier recovery methods using MATLAB.

Minor Project: To be assigned by concerned instructor/course-coordinator
**Course Learning Outcomes (CLOs):**

1. To understand Optimum Receivers for AWGN Channels.
2. To understand the pass band communication and modulation techniques to understand the small scale fading models.
3. To understand the concept of Carrier and Symbol Synchronization.
4. To understand the concept of ISI and its removal.
5. To understand the concept of communication in band limited channels.

**Text Books:**


**Reference Books:**


**Evaluation Scheme:**

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PWC203: ADVANCED WIRELESS COMMUNICATION SYSTEMS

L  T  P  Cr
3  1  2  4.5

Course Objective: To understand the fundamentals of wireless communication, wireless channel modeling (large scale and small scale). Calculate the capacity of wireless channels along with performance of digital modulation techniques over wireless fading channels.


Small Scale Fading Model: Time-Varying Channel Impulse Response, Autocorrelation, Cross Correlation, and Power Spectral Density Level Crossing Rate and Average Fade Duration, Finite State Markov Channels, Wide band Fading Models, Power Delay Profile, Coherence Bandwidth, Doppler and Channel Coherence Time, Transforms for Autocorrelation and Scattering Functions, Discrete-Time Model, Space-Time Channel Models

Capacity of Wireless Channels: Capacity in AWGN, Capacity of Flat-Fading Channels, Channel and System Model, Channel Distribution Information (CDI) Known, Channel Side Information at Receiver, Channel Side Information at Transmitter and Receiver, Capacity with Receiver Diversity, Capacity Comparisons, Capacity of Frequency-Selectable Fading Channels.

Diversity: Diversity techniques for binary signals, multiphase signals, M-ary orthogonal signals on multipath channel, Receiver Diversity, System Model, Combining techniques, Moment Generating Functions in Diversity Analysis for MRC, EGC, SC of Non-coherent and Differentially Coherent Modulation

Multicarrier Modulation: Data Transmission using Multiple Carriers, Overlapping Subchannels, Mitigation of Sub carrier Fading, Discrete Implementation of Multi-carrier, Cyclic Prefix, OFDM, Matrix Representation of OFDM, Vector Coding, PAR, Frequency and Timing Offset, Multi-user Channels, Multiple Access, Downlink Channel Capacity, Uplink Channel Capacity, Capacity in AWGN, Fading, and with Multiple Antennas.

Course Learning Outcomes (CLO):
At the end of this course, the students should be able to:
1. Understand the fundamentals and advancement in wireless communication systems.
2. Understand the modeling (large scale and small scale) of wireless Channel.
3. Evaluate the performance of digital modulation techniques in wireless environment.
Recommended Books:

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PEC106: OPTICAL COMMUNICATION NETWORKS

L  T  P  Cr
3  0  2  4.0

Course Objective: The main objective of this course is to understand the concept of optical networks in optical communication systems. In this syllabus, Introduction to optical networks and its enabling technologies such as transmitters, optical receivers, filters, optical amplifiers, WDM network elements and their designs are discussed. Free space optics and its use in making optical networks is of great concern in today’s optical communication systems. Moreover, Control and management, network survivability, optical TDM and CDM networks are discussed as well.


Optical TDM and CDM Networks: Optical TDM Networks, Optical CDM Networks.

Laboratory Work: Basic optical communication link experiments, DWDM experiments, Amplifier, Splicing, and OTDR experiment, System design and performance analysis using simulation tools.

Minor Project: To be assigned by concerned instructor/course-coordinator

Course Learning Outcome (CLOs):
1. Ability to identify, formulate and solve optical communication networks related problems using efficient technical approaches.
2. Ability to design optical networks as well as to interpret statistical and physical data.
3. Ability to design and implement WDM networks.
4. Ability to control and manage the functions of optical networks.
5. Understanding the network survivability by various protection schemes.

**Text Books:**

**Reference Books:**

**Evaluation Scheme:**

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PEC393: IMAGE PROCESSING AND COMPUTER VISION

Course Objective: To make students to understand image fundamentals and how digital images can be processed, Image enhancement techniques and its application, Image compression and its applicability, fundamentals of computer vision, geometrical features of images, object recognition and application of real time image processing.

Introduction: Digital image representation, fundamental steps in image processing, elements of digital image processing systems digitization.


Image Enhancement: Histogram processing, image subtraction, image averaging, smoothing filters, sharpening filters, enhancement in frequency and spatial domain, low pass filtering, high pass filtering.


Real Time Image Processing: Introduction to Digital Signal Processor (TMS320CXX), Introduction to Texas Instruments Image Library, Development of a real time image processing algorithms.

Computer Vision: Imaging Geometry, Coordinate transformation and geometric warping for image registration, Hough transforms and other simple object recognition methods, Shape correspondance and shape matching, Principal Component Analysis, Shape priors for recognition, Implementation of computer vision algorithms using Raspberry Pi.

Laboratory Work:
1. Introduction to Image Processing Toolbox of Python and MATLAB®.
2. Sampling and Quantizing Images.
4. Filtering of Images.
5. Geometrical transformations on Images.


Course Learning Outcomes (CLOs):
1. Understanding of fundamental techniques of Image Processing and Computer Vision.
2. Understanding and acquiring the basic skills of designing image compression.
3. Familiarizing himself/herself with image compression standards.
4. Familiarizing himself/herself with computer vision.
5. Familiarizing himself/herself with real time image processing.
Text Books:

Reference Books:

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Course Objectives: To understand the theory, operation, device fabrication and the C-V, V-I and frequency response characteristics of RF devices and their application in latest engineering circuits. Some of these devices are SBD, Tunnel diode and advanced devices like IMPATT, TRAPATT and HEMT have been included in this course.


Varactor Diode: Diode theory based on the ideality factor and its use in diode equation for generating various equations including normal, linear, abrupt and hyper abrupt junctions, their operation and frequency response, The device capacitance equations for these diodes and their applications in high frequency circuits.

The p-i-n Diode: p-i-n structure, device theory. Equation for drift region, carrier concentration n(x) as a function of x in the intrinsic region using the equation of continuity; the I-region capacitance. Equivalent circuit of a packaged p-i-n diode both in forward and reverse bias. Application of p-i-n diode as a switch using the concept of insertion loss and isolation based on the conductance and susceptance of a transmission line and its operation, and for current limiter using a strip line cross section for analysis of limiting action in microwave range.

The IMPATT Diode: IMPATT structure; theory of IMPATT diodes using the equation for junction breakdown, equivalent circuit of IMPATT diode for LC avalanche region, drift region and the parasitic resistance to derive the device impedance. Active resistance and its use for generation of negative resistance, interchange of L & C components of the avalanche region at the output frequency. Frequency – power curve of IMPATT diode, IMPATT mountings.

The TRAPATT Diode: The TRAPATT structure and the theory of operation. Concept of carrier velocities exceeding saturated drift velocity of carriers in the central region of the device. The electric field, distance and time, i.e., 3-axis plot of the diode with bias. Output voltage and current waveforms plots, power and frequency limitations of TRAPATT diode and the related duty cycle.

The Gunn Oscillator: Transferred Electron Devices or Bulk Effect Devices. E-k diagrams, velocity field profiles of semiconductors, RWH mechanism for mass variation with electric field IN-semiconductors, threshold field for negative differential resistance (NDR), dipole domain formation, Gunn Effect, Different modes of operation, Gunn, LSA and Quenched Domain mode. The output power and frequency of Gunn Oscillators.
**Tunnel Diode:** Degeneratively doped diode and their energy band diagrams, V-I characteristics and the generation of negative resistance in tunnel diodes, Tunnel diode as a switch and its operation as a MW generator.

**Step Recovery Diode:** SRD device structure and operation, Application of SRD as a harmonic generator in a MW range, SRD theory, carrier transit times in SRD, Operation of SRD multiplier using bias method and general impedance matching method.

**Microwave Transistor:** Structure and design of Bipolar MW Transistors, Equivalent circuit of a packaged MW transistor and associated S parameters, Device geometry, cutoff frequency and operation of MESFET and HEMT.

**Semiconductor Heterojunctions:** Basic device model, Energy band diagram of Semiconductor Heterojunction diodes and their V-I characteristics, Introduction to Heterojunction transistors and lasers.

**Fundamentals of Power Semiconductor devices:** Introduction, SiC Material properties, polytypes, comparison of electrical properties of polytypes; Transport physics of SiC power devices, Breakdown voltage, SiC Schottky Rectifiers, SiC Metal-Semiconductor Field Effect Transistors (DIMOSFET and LDMOSFET).

**Minor Project:** To be assigned by concerned instructor/course-coordinator

**Course Learning Outcomes (CLOs):**
1. Understand semiconductor device theory at an advanced level including the use of energy band diagram as applied to devices like BJT and MOSFETs.
2. Solve device equations based on equations of continuity and the derivation of C-V and I-V equations of High Frequency devices.
3. Understand and develop the equivalent circuit of High Frequency devices and simplify them for analytical work.
4. Understand the fabrication of devices like SBD, Tunnel diode, DIMOSFET and SiC power devices.

**Text Books:**

**Reference Books:**
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Course Objective: To know how to design high-speed, low-area, and low-power VLSI systems for a broad range of DSP applications. Explore optimization techniques indispensable in modern VLSI signal processing. Immediate access to state-of-the-art, proven techniques for designers of DSP applications-in wired, wireless, or multimedia communications.


Algorithmic Transformations: Retiming, Cut-set retiming, Feed-Forward and Feed-Backward, Clock period minimization, register minimization, Unfolding, Sample period reduction, Parallel processing, Bit-serial, Digit-serial and Parallel Architectures of DSP Systems, Folding, Folding order, Folding Factor, Folding Bi-quad filters, Retiming for folding, Register Minimization technique, Forward Backward Register Allocation technique.

Systolic Architecture Design and Fast Convolution: Systolic architecture design methodology, Projection vector, Processor Space vector, Scheduling vector, Hardware Utilization efficiency, Edge mapping, Design examples of systolic architectures, Cook-Toom Algorithm and Modified Cook-Toom Algorithm, Wniograd Algorithm and Modified Winograd Algorithm, Iterated Convolution, Cyclic Convolution.


Course Learning Outcomes (CLO):
1. To learn performance optimization techniques in VLSI signal processing,
2. Transformations for high speed and power reduction using pipelining, retiming, parallel processing techniques, supply voltage reduction as well as for strength or capacitance reduction,
3. Area reduction using folding techniques, Strategies for arithmetic implementation,
4. Synchronous, wave, and asynchronous pipelining.

Text Books:

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Course Objectives: In this course the students will learn overview of nanoelectronic and nano devices, its mechanics and technologies, nano fabrication and characterization and its future aspects.

Shrink-down approaches: Introduction to Nanoscale Systems, Length energy and time scales, Top down approach to Nanolithography, CMOS Scaling, Limits to Scaling, System Integration Limits - Interconnect issues, etc.

Overview of Nanoelectronics and Devices: The Nano-scale MOSFET, FinFETs, Vertical MOSFETs, Resonant Tunneling Transistors, Single Electron Transistors, New Storage devices, Optoelectronic and Spin electronics Devices.

Basics of Quantum Mechanics: History of Quantum Mechanics, Schrödinger Equation, Quantum confinement of electrons in semiconductor nano structures, 2D confinement (Quantum Wells), Density of States, Ballistic Electron Transport, Coulomb Blockade, NEGF Formalism, Scattering.


Nano-Fabrication and Characterization: Fabrication: Photolithography, Electron-beam Lithography, Advanced Nano-Lithography, Thin-Film Technology, MBE, CVD, PECVD

Characterization: Scanning Probe Microscopy, Electron Microscopy (TEM, SEM), Photon Spectroscopy, Nano Manipulators

Future Aspects of Nanoelectronics: Molecular Electronics: Molecular Semiconductors and Metals, Electronic conduction in molecules, Molecular Logic Gates, Quantum point contacts, Quantum dots and Bottom up approach, Carbon Nano-tube and its applications, Quantum Computation and DNA Computation. Overview of Organic Electronics: OLEDs, OLETs, Organic Solar Cells

Course Learning Outcomes (CLO):
After the completion of this course, the students are able to:
1. acquire knowledge about nanoelectronics and shrink down approach.
2. understand concept behind nanomosfets and nano devices.
3. set up and solve the Schrödinger equation for different types of potentials in one dimension as well as in 2 or 3 dimensions for specific cases.
4. use the nanofabrication and characterization facilities.
**Recommended Books:**


**Evaluation Scheme:**

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PEC327: PHOTONICS INTEGRATED DEVICES AND CIRCUITS

Course Objectives: In this course the students will learn basics of optical fiber communication, optical waveguides, light sources, amplifiers, modulators, detectors, optical MEMS & NEMS and silicon photonics.

Introduction to Optical Fiber Communication: Nature of light; optical communication; optical fibers; propagation of light in optical fibers; transmission characteristics of optical fibers; fabrication of optical fibers.


Semiconductor Light Sources and Amplifiers: Spontaneous and stimulated emission, emission from semiconductors, semiconductor injection lasers, single frequency lasers, Various laser configurations, injection laser characteristics, VCSEL, LEDs - Introduction, LED power efficiency, LED structures, LED characteristics and Organic LEDs, Optical amplifiers, Semiconductor optical amplifier.


Optical Detectors: Optical detection principle, quantum efficiency and responsivity, semiconductor photodiodes with/without internal gain, Solar cell.

Optical MEMS and NEMS: Micro-electro-mechanical and nano-electro-mechanical systems, MEMS integrated tunable photonic devices-filters, lasers, hollow waveguides; NEMS tunable devices


Course Learning Outcomes (CLO):
After the completion of this course, the students are able to:
1. understand the fundamentals, advantages and advances in optical communication and integrated photonic devices and circuits.
2. introduce optical waveguides, detectors, amplifiers, silicon photonics and MEMS applications in photonics.
3. design, operate, classify and analyze Semiconductor Lasers, LEDs, modulators and other Integrated photonic devices.
4. identify, formulate and solve engineering-technological problems related optoelectronic integration.
Recommended Books:

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**Course Objective:** To understand the performance of MIMO system, MIMO-OFDM system, space time block codes (STBC), Alamouti schemes of channel estimation, space time Trellis codes, and other various space time coding schemes and their performances analysis.


**Performance Limits Of Multiple-Input Multiple-Output Wire Less Communication Systems:** MIMO System Model, Capacity in AWGN, Channel Side Information at Receiver, Channel Side Information at Transmitter and Receiver, Capacity of Frequency-Selective Fading MIMO System Capacity Derivation, Capacity of MIMO Systems with Random Channel Coefficients Channels, Capacity of MIMO Systems with Static , Capacity of MIMO Systems with Fading Channels

**Multiple Antennas and Space-Time Communications:** Narrowband MIMO Model, Parallel Decomposition of the MIMO Channel MIMO Diversity Gain: Beamforming, Diversity/Multiplexing Tradeoffs, Space-Time Modulation and Coding. ML Detection and Pair Wise Error Probability

**Space-Time Block Codes:** Alamouti Space-Time Code with Multiple Receive Antennas, Space-Time Block Codes (STBC), STBC for Real Signal Constellations, STBC for Complex Signal Constellations, Decoding of STBC, Performance of STBC, Effect of Imperfect Channel Estimation and Antenna Correlation on Performance

**Layered Space-Time Codes:** LST Transmitters, LST Receivers, QR Decomposition, Interference Minimum Mean Square Error (MMSE) Suppression Combined with Interference Cancellation, Iterative LST Receivers, An Iterative Receiver with PIC, An Iterative MMSE Receiver, Comparison of the Iterative MMSE and the Iterative PIC-DSC Receiver, VBLAST architecture, DBLAST Architecture.

**Space-Time Trellis Codes:** Encoder Structure for STTC, Generator Description, Optimal STTC Based on the Rank, Determinant and Trace Criterion, Performance Comparison for Codes Based on Different Design Criteria, The Effect of Imperfect Channel Estimation on Code Performance, Design of Space-Time Trellis Codes on Fast Fading Channels, Construction of Recursive STTC,
**Course Learning Outcomes (CLO):**
At the end of the course, the students should be able to:

1. Understand the basic concepts of space time coding techniques and their used in MIMO and MIMO-OFDM system.
2. Evaluate the performance of various space time coding schemes in different fading channel scenario.
3. Solve the engineering problems related to space time coding using in MIMO-OFDM system in different fading channels.

**Recommended Books:**

**Evaluation Scheme:**

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PWC212: WIRELESS SECURITY

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**Course Objectives:** To gain and understand the complete knowledge of threats within wireless environments. To recognize typical vulnerabilities and safeguards for wireless communication to include: Cellular and Personal Communications Services (PCS) network security, secure wireless encrypted e-mail solution, Wireless handheld device security, PAN and LAN security.


**Wireless Physical Layer Technologies** ISM Spectrum, Frequency Hopping Spread Spectrum (FHSS), Direct Sequence Spread Spectrum (DSSS), Orthogonal Frequency Division Multiplexing (OFDM).

**Wireless Local and Personal Area Networks** Ad Hoc Mode, Infrastructure Mode, Bridging, Repeater, Mesh Wireless Networks, Local Area Networking Standards, IEEE 802.11, Real-World Wireless Data Rates, Personal Area Network (PAN) 802.15, Bluetooth 802.15.1, Infrared (IR), Ultra wide Band 802.15.3, ZIGBEE 802.15.4

**Wide Area Wireless Technologies:** Cell Phone Technologies, Analog, TDMA, CDMA, CDMA2000, GSM, GPS, 802.16 Air Interface Standards, 802.20 Standards.

**The Wireless Deployment Process:** Gather Requirements, Estimation. Make the Business Case, Site Survey

**Course Learning Outcomes (CLO):**

At the end of the course, the students should be able to:

1. Understand the basic concepts of wireless security and methods to achieve it.
2. Understand the process of data hiding and its utility in wireless communication.
3. Encryption and decryption of data using optimal tools/techniques.
4. Understand the various model and their parameters on which performance of network depends in communication.

**Recommended Books:**

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PWC202: ADVANCED WIRELESS NETWORKS

L T P Cr
3 0 0 3.0

Course Objectives: Build an understanding of the fundamental concepts of wireless networking. Familiarize the student with the basic taxonomy and terminology of the various channel modeling and layers of the network. Introduce the student to resource management and security issues in wireless networks. Allow the student to gain expertise in some specific areas of networking such as Ad Hoc networks, sensors networks, and active networks.


Mobility and Resource Management: Prioritized Handoff, Cell Residing Time Distribution, Mobility Prediction in Pico- and Micro-Cellular Networks, Channel Assignment Schemes, Resource Management in 4G.


**Course Learning Outcomes (CLO):**
Having successfully completed this course, the students will:
1. Demonstrate their ability to perform channel modeling for 4G wireless systems
2. Demonstrate advanced knowledge of networking and wireless networking in particular
3. Demonstrate knowledge of protocols and programming used in wireless communications

**Recommended Books**
4. Rappaport, T.S., *Wireless Communications*

**Evaluation Scheme:**

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PEC211: PASSIVE OPTICAL NETWORKS

Course Objective: In this course the students will learn the basic optical networks design using point-to-point fiber links, star, bus and ring topologies, multiple access techniques such as WDM, SONET, PON widely used with FTTH schemes and emerging ROF networks that bridge the optical and wireless networks.


Components for Future Access Networks: Tuneable Optical Network Unit, Fast-Tunable Laser at the Optical Line Terminal, Arrayed Waveguide Gratings, Reflective Receivers and Modulators, Colourless ONT.


Economic Models: WDM/TDM PON, Long Reach PONs, Long Term Dynamic WDM/TDM-PON Cost Comparison.

Minor Project: To be assigned by concerned instructor/course-coordinator
**Course Learning Outcomes (CLOs):**
After completing this course student will be able to
1. Understand and evaluate the performance of various enabling technologies used in modern optical networks.
2. Evaluate different WDM network topologies including broadcast-and-select and wavelength routing networks.
3. Design virtual WDM network topologies.

**Text Books:**

**Reference Books:**

**Evaluation Scheme:**

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PEC212: AUDIO AND SPEECH PROCESSING

Course Objective: This course will give students a foundation in current audio and recognition technologies, familiarity with the perceptually-salient aspects of the speech signal, its processing, speech pattern recognition and speech and audio recognition systems.


Digital Models for Speech signals: Speech production and acoustic tube modeling, acoustic phonetics, anatomy and physiology of the vocal tract and ear, hearing and perception.

Digital Representation: Linear quantization, commanding, optimum quantization, PCM, effects of channel errors, vector quantization (VQ), Adaptive quantization, differential PCM, APCM ,ADPCM, delta modulation, adaptive delta modulation, and CVSD.

Digital Vocoder: Linear predictive coding (LPC), hybrid coders: voice excited vocoders, voice excited linear predictor, and residual excited linear predictor (RELP).

Speech Recognition: Isolated word recognition, continuous speech recognition, speaker (in) dependent, measures and distances (articulation index, log spectral distortion, Itakura-Saito, cepstral distance), Dynamic time warping (DTW), HMM, HMM networks, Viterbi algorithm, discrete and continuous observation density HMMs.

Speaker Recognition: speaker verification/authentication vs. speaker identification, closed vs. open set, feature vectors (e.g., line spectrum pair and cestrum), pattern matching (e.g., DTW, VQ, HMM), hypothesis testing, and errors.

Advanced Topics: Emerging speech coding standards (e.g., 2400 bps MELP), Internet phone, voice and multimedia applications, audio signal generation, speech generation and recognition algorithms and techniques using MATLAB and related DSP kits.

Minor Project: To be assigned by concerned instructor/course-coordinator

Course Learning Outcomes (CLOs):
1. Acquired knowledge about audio & speech signals.
2. Ability to understand speech generation models.
3. Foster ability to understand speech recognition models.
4. Understanding of audio & speech signal estimation & detection.
5. Acquired knowledge about hardware to process audio & speech signals.
6. Ability to relate human physiology and anatomy with signal processing paradigms.
Text Books:

Reference Books:

Evaluation Scheme:

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PEC215: DETECTION AND ESTIMATION THEORY

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Course Objective: Comprehensive understanding of the detection, parameter estimation, and signal estimation (filtering) theory based on the observations of the continuous-time and discrete-time signals. This will acquaint the students to apply this theory in varied applications spanning from radar/sonar processing, speech processing to signal/image analysis and more. In depth detailed analytical aspects of designing and analysing various optimum detection and estimation schemes with real life examples. To familiarize the understanding of white noise and colored noise and understand the concept of suboptimum and adaptive receivers. To get familiarize with the doppler-spread targets and channels and examine the performance of the optimum receiver and the communication over doppler-spread channels.


Doppler-Spread Targets and Channels: Model for Doppler-Spread Target (or Channel), Detection of Doppler-Spread Targets, Likelihood Ratio Test, Canonical Receiver Realizations, Performance of the Optimum Receiver, Communication Over Doppler-Spread Channels, Performance Bounds for Optimized Binary Systems, Doppler-Spread Target, Detection of Range-Spread Targets, Time-Frequency Duality, Dual Targets and Channels, Model for a
Doubly-Spread Target, Differential-Equation Model for a Doubly-Spread Target (or Channel), Detection in the Presence of Reverberation or Clutter (Resolution in a Dense Environment), Detection of Doubly-Spread Targets and Communication over Doubly-Spread Channels, Approximate Models for Doubly-Spread Targets and Doubly-Spread Channels, Binary Communication over Doubly-Spread Channels, Detection under LEC Conditions, Parameter Estimation for Doubly-Spread Targets, Estimation under LEC Conditions, Amplitude Estimation, Estimation of Mean Range and Doppler.

**Minor Project:** To be assigned by concerned instructor/course-coordinator

**Course Learning Outcomes (CLOs):**

1. Introduce the fundamental concepts of detection and estimation theory involving signal and system models in which there is some inherent randomness and to investigate how to use tools of probability and signal processing to estimate signals and parameters.
2. To identify the optimal estimator/detector or at least bound the performance of any estimator/detector and to study various linear and nonlinear estimation techniques for the detection and estimation of signals with and without noise.
3. To investigate the analytical aspects of various optimum filters/receivers with their system realization and also study various adaptive filters and their mathematical models for detection of Gaussian signals.
4. To focus on the concept of white and colored noise with their finite state representation. Also, study is to be done on the time-frequency signal analysis and processing with their various mathematical distribution tools.
5. To investigate the detection of Doppler-spread targets and the canonical receiver realizations, alongwith the performance of the optimum receiver. Also, study about the models for doubly-spread targets and channels.

**Text Books:**


**Reference Books:**


**Evaluation Scheme:**

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PEC216: ADVANCED COMPUTER NETWORKS AND PROTOCOLS  

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Course Objective: The objective of this course unit is to study the problematic of service integration in TCP/IP networks focusing on protocol design, implementation and performance issues; and to debate the current trends and leading research in the computer networking area.


Minor Project: To be assigned by concerned instructor/course-coordinator

Course Learning Outcomes (CLOs):

Upon the completion of the course the student will

1. Acquire knowledge about Network Fundamentals.
2. Be able to identify Internetworking.
3. Foster ability to work using Network Standards and Standard Organizations.
4. Foster ability to work using TCP/IP Network Interface Layer Protocol.
5. Acquire knowledge about Routing and Application Layer Protocols.

Text Books:

Reference Books:

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Course Objective: To familiarize students with the fundamentals of operating, architecture, interfacing and analyzing real time digital signal processing systems, including the required theory. The hardware used to sample and process the signals, and real time software development environments.

An Introduction to DSP Processors: Advantages of DSP, characteristics of DSP systems, classes of DSP applications, DSP processor embodiment and alternatives, Fixed Vs Floating point processors, fixed point and Floating point Data Paths.

DSP Architecture: An introduction to Harvard Architecture, Differentiation between Von-Neumann and Harvard Architecture, Quantization and finite word length effects, Bus Structure, Central Processing Unit, ALU, Accumulators, Barrel Shifters, MAC unit, Compare, Select, and store unit (CSSU), Data addressing and program memory addressing.

Memory Architecture: Memory structures, features for reducing memory access required, wait states, external memory interfaces, memory mapping, data memory, program memory and I/O memory, memory mapped registers.

Addressing: Various addressing modes: implied addressing, immediate data addressing, memory direct addressing, register direct and indirect addressing, and short addressing modes.

Instruction Set: Instruction types, various types registers, orthogonality, assembly language and application development.

Execution Control and Pipelining: Hardware looping, interrupts, stacks, pipelining and performance, pipelining depth, interlocking, branching effects, interrupt effects, instruction pipelining.

Peripherals: Serial ports, timers, parallel ports, bit I/O ports, host ports, communication ports, on-chip A/D and D/A converters, external interrupts, on chip debugging facilities, power consumption and management.

Processors: Architecture and instruction set of TMS320C3X, TMS320C5X, TMS320C6X, ADSP 21XX DSP Chips, some example programs.


Laboratory Work: 
Introduction to code composer studio, Using CCS write program to compute factorial, dot product of two arrays, Generate Sine, Square and Ramp wave of varying frequency and
amplitude, Design various FIR and IIR filters, Interfacing of LED, LCD, Audio and Video Devices with the DSP processor.

**Minor Project:** To be assigned by concerned instructor/course-coordinator

**Course Learning Outcomes (CLOs):**
1. Acquired knowledge about Fixed and floating point number systems.
2. Ability to understand the internal Structures of DSP Processors and memory accesses.
3. Ability to understand addressing instructions of a DSP processors.
4. Foster ability to understand the internal architecture, instructions set, programming and interfacing of different peripheral devices with TMS320C3X, TMS320C5X, TMS320C6X, ADSP 21XX DSP Chips.

**Text Books:**

**Reference Books:**
2. TI DSP reference set (www.ti.com).

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PEC219: MULTIMEDIA COMPRESSION TECHNIQUES

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**Course Objective:** To provide the foundation knowledge of multimedia computing, e.g. media characteristics, compression standards, multimedia representation, Data formats, multimedia technology development and to provide programming training in multimedia computing, Multimedia system design and implementations.

**Human’s Visual and Audio system:** Characteristics of human visual system, Light and visible light, human retina structure and functions, Non-perceptual uniform color models and perceptual uniform color models, Characteristics of human’s audio system, Frequency response and magnitude range.

**Multimedia Data Representation and Analysis:** Representation of sound/audio, Image and video,speech generation, Analysis and software, Image analysis, Display, and printing.

**Text Coding:** Lossless JPEG, UNIX compress, and the GIF format, Burrows-Wheeler compression, Gunzip, Winzip etc.

**Speech Compression:** Speech Production model, Objectives and requirements of speech coding, Quantizers for speech signal, Differential PCM and adaptive prediction, Linear predictive coding(LPC) of speech, Computational aspects of LPC parameters, Cholesky decomposition, Lattice formulation of LPC parameters, Linear predictive synthesizers, LPC Vocoder, Code excited linear predictive coding, Voice excited linear predictive coding.

**Image Compression:** Introduction, Lossless and Lossy image compression, Discrete Cosine Transform (DCT), DCT Quantization and limitations, Theory of wavelets, Discrete wavelet transforms (DWT), DWT on images and its encoding, Embedded Zero Tree wavelet encoding, Digital watermarking, Introduction to Curve-lets.

**Video Coding:** Video coding and various building blocks, Motion estimation techniques, Fast motion estimation techniques, Video coding standards, Advanced coding aspects, Introduction to VoIP, VoIP signaling, H.323 Protocol, H.323 multiplexing, Header compression and bandwidth, ISDN Video conferencing, Features and design issues of H.264 video standards, Video Conferencing, SIP protocol.

**Multimedia Technology Development:** Multimedia history, technology development, challenging problem, research difficulty, multimedia industry.

**Minor Project:** To be assigned by concerned instructor/course-coordinator

**Course Learning Outcomes (CLOs):**
At the end of the course, the students should be able to:

1. Understand and develop human speech mode, understand characteristics of human’s visual system, understand the characteristics of human’s audio system.
2. Understand different compression principles, understand different compression techniques, understand different multimedia compression standards, be able to design and develop multimedia systems according to the requirements of multimedia applications.
3. Understand the various signal processing aspects of achieving high compression ratios.
4. Understand and develop new paradigm technologies in audio and video coding.
5. Understand the application of modern multimedia compression techniques in the development of new wireless communication protocols.

**Text Books:**

**Reference Books:**

**Evaluation Scheme:**

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Course Objective: To introduce time frequency analysis, Study of Discrete Fractional Fourier transforms, Applications of Fractional Fourier Transform in Optics and signal processing, to introduce various other forms of Fractional Transform.

Introduction: Fractional operations and the fractional Fourier transform, Applications of the fractional Fourier transform, Signals, Systems, Representations and transformations, Operators, The Fourier transform, Some important operators, Uncertainty relations, Time-frequency and space-frequency representations, The Wigner distribution and the ambiguity function, Linear canonical transforms.

The Fractional Fourier Transform: Fractional operations, Definitions of the fractional Fourier transform, Eigen-values and Eigen-functions, Transforms of some common functions, Properties, Rotations and projections in the time-frequency plane, Fractional Fourier domains, Chirp bases and chirp transforms, Relationships with the Wigner distribution and the ambiguity function, Two-dimensional fractional Fourier transforms, Applications of the fractional Fourier transform.

The Discrete Fractional Fourier Transform: Discrete Hermite-Gaussian functions, the discrete fractional Fourier transform, Definition in hyper difference form, Higher-order discrete analogs, Discrete computation of the fractional Fourier transform.

The Fractional Fourier Transform in Optics: General fractional Fourier transform relations in free space, Fractional Fourier transformation in quadratic graded-index media, Hermite-Gaussian expansion approach, First-order optical systems, Fourier optical systems, Locations of fractional Fourier transform planes, Wave-field reconstruction, phase retrieval, and phase-space tomography, Applications of the transform to wave and beam propagation.

Applications to Signal Processing: Optimal Wiener filtering in fractional Fourier domains, Multi-stage, multi-channel, and generalized filtering configurations, Applications of fractional Fourier domain filtering, Convolution and filtering in fractional Fourier domains, Repeated filtering in the ordinary time and frequency domains, Multiplexing in fractional Fourier domains, Fractional correlation, Controllable shift-invariance, Performance measures for fractional correlation, Fractional joint-transform correlators, Adaptive windowed fractional Fourier transforms, Applications with different orders in the two dimensions.

Other fractional Transforms: Fractional sine and Cosine transforms fractional Hartley Transforms, fractional Wavelet Transforms and their applications in one and two dimensional Signal processing.

Minor Project: To be assigned by concerned instructor/course-coordinator.
Course Learning Outcomes (CLOs):
1. To understand Time frequency analysis of signals.
2. To learn concepts of Fractional Fourier Transform.
3. To introduce various applications of Fractional Transform.
4. To learn different types of Fractional Fourier Transforms.

Text Books:

Reference Books:
1. IEEE and Elsevier Papers

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PEC326: OPTOELECTRONICS

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**Course Objective:** To understand the nature of light, fundamentals, advances and applications of optoelectronic materials, devices and systems; to understand fundamentals of optoelectronic properties of semiconductors, to analyze optoelectronic system based on important parameters for characterizing optical fiber, optical source, detector and amplifier, fundamentals and advances in lasers, LEDs, photodiodes, optical and optical-electronic modulators, optical-filters, displays, memory, basics of optical fiber communications, optical measurements and sensors.


**Photo Detectors:** Principle of optical detection, Detector performance parameters, Thermal detectors, Photon devices, Solar cell.

**Display Devices:** Luminescence, Photoluminescence, Cathodoluminescence, Cathode ray tube, Electro luminescence, Injection luminescence and light emitting diodes, Plasma displays, Display brightness, LCD, Numeric displays.

**Optical Fiber Communication:** Optical Communication, Total internal reflection, Planar dielectric waveguide, Optical fiber waveguide, Losses in fibers, Optical fiber connectors, Measurement of fiber characteristics, Fiber materials and manufacturing, Fiber cables, Modulation schemes, Free space communication, Fiber optical communication systems, Integrated optics.

**Optical Measurements:** Optical fiber sensor, Optical CDs.

**Minor Project:** To be assigned by concerned instructor/course-coordinator.
Course Learning Outcomes (CLOs):
1. Able to understand fundamentals, advantages and advances in optoelectronic devices, circuits and systems.
2. To acquire a detailed understanding of types, basic properties and characteristics of optical waveguides, modulators and detectors.
3. Able to have the knowledge of design, working, Classification and analysis of Semiconductor Lasers, LEDs, and modulators.
4. Able to identify, formulate and solve engineering and technological problems related to optical sources, displays, detectors and optical measurements.

Text Books:

Reference Books:

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PEC222: HDL AND SYSTEM C PROGRAMMING

Course Objective:
1. Students must demonstrate the use and application of Boolean Algebra in the areas of digital circuit reduction, expansion, and factoring.
2. Students must learn the IEEE Standard 1076 VHDL Hardware Description Language.
3. Students must be able to simulate and debug digital systems described in VHDL.
4. Students must be able to synthesize complex digital circuits at several level of abstractions.

VHDL:
Combinational Logic: Design units, entities and architectures, simulation and synthesis model, signals and ports, simple signal assignments, conditional signal assignments, selected signal assignment.

Types: standard types, standard operators, scalar types, records, arrays.

Operators: standard operators, operator precedence, Boolean operators, comparison operators, arithmetic operators, concatenation operators, mixing types in expressions, numeric packages.

Sequential VHDL: Processes, signal assignments, variables, if statements, case statements.

Hierarchy: Role of components, using components, component instances, component declaration, Configuration specifications, default binding, binding process, component packages, generate statements.

Subprograms: Functions, type conversions, procedures, declaring subprograms.

Test Benches: Test benches, verifying responses, clocks and resets, printing response values.

SystemC: Overview: Capabilities, Design Hierarchy, Data Types, Modelling combinational Logic, Modelling Sequential Logic, Writing Testbenches.

Laboratory Work:
Modeling and simulation of all VHDL and SystemC constructs using ModelSim, their testing by modeling and simulating test benches.

Minor Project: To be assigned by concerned instructor/course-coordinator

Course learning outcome (CLOs):
1. Model digital systems in VHDL and SystemC at different levels of abstraction.
2. Partition a digital system into different subsystems.
3. Simulate and verify a design.
4. Transfer a design from a version possible to simulate to a version possible to synthesize.
5. Use modern software tools for digital design in VHDL.
Text Books:

Reference Books:

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Course Objectives: In this course the students will learn the basics of VLSI design for high speed processing, methods for logical efforts, logic styles, latching strategies, interface techniques and related issues.

Introduction of High Speed VLSI Circuits Design

Back-End-Of-Line Variability Considerations: Ideal and non ideal interconnect issues, Dielectric Thickness and Permittivity

The Method of Logical Effort: Delay in a logic gate, Multi-stage logic networks, Choosing the best number of stages.

Deriving the Method of Logical Effort: Model of a logic, Delay in a logic gate, Minimizing delay along a path, Choosing the length of a path, Using the wrong number of stages, Using the wrong gate size.


Circuit Design Margining: Process Induced Variations, Design Induced Variations, Application Induced Variations, Noise

Latching Strategies: Basic Latch Design, Latching single-ended logic, Latching Differential Logic, Race Free Latches for Pre-charged Logic Asynchronous Latch Techniques


Clocking Styles: Clock Jitter, Clock Skew, Clock Generation, Clock Distribution, Asynchronous Clocking Techniques.

Skew Tolerant Design.

Course Learning Outcomes (CLO):

After the completion of this course, the students are able to:
1. acquire knowledge about High Speed VLSI Circuits Design.
2. identify the basic Back-End-Of-Line Variability Considerations.
3. use the Method of Logical Effort.
4. model the Circuit Design Margining and Latching Strategies.
5. understand the Clocking Styles.
Recommended Books:

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PVL332: MIXED SIGNAL CIRCUIT DESIGN

Course Objectives: In this course the students will learn basics of comparator circuits, data converters, implementation of A/D and D/A converters and their performance analysis with design challenges.


Data Converters: Requirements, Static and Dynamic Performance, SNR and BER, DNL, INL.

High Speed A/D Converter Architectures: Flash, Folding, Interpolating, pipelined

High Speed D/A Converter Architectures: Nyquist-Rate D/A Converters, Thermometer Coded D/A Converters, Binary Weighted D/A Converters.

Design of multi channel low level and high level data acquisition systems using ADC/DAC, SHA and Analog multiplexers, Designing of low power circuits for transducers.

Sigma-Delta Data Converter Architectures: Programmable Capacitor Arrays (PCA), Switched Capacitor converters, Noise Spectrum, Sigma-Delta Modulation Method, Sigma-Delta A/D and D/A Converters, Non Idealities.

Key Analog Circuit Design: Analog VLSI building blocks, Operational Amplifiers for converters, advanced op-amp design techniques, Voltage Comparators, Sample-and-Hold Circuits.


Course Learning Outcomes (CLO):
After the completion of this course, the students are able to:
1. apply knowledge of mathematics, science, and engineering to design CMOS analog circuits to achieve performance specifications.
2. identify, formulates, and solves engineering problems in the area of mixed-signal design.
3. use the techniques and skills for design and analysis of CMOS based switched capacitor circuits.
4. work as a team to design, implement, and document a mixed-signal integrated circuit.

**Recommended Books:**

**Evaluation Scheme:**

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PVL343: SENSOR TECHNOLOGY AND MEMS

Course Objectives: In this course the students will learn basic concept of MEMS devices, their working principles, equivalent circuits, different MEMS sensors, fabrication technologies, modeling and characterization tools and calibration techniques.

Introduction to MEMS: Introduction to MEMS and Micro sensors, MEMS system-level design methodology, Equivalent Circuit representation of MEMS, Signal Conditioning Circuits.

Principles of Physical and Chemical Sensors: Sensor classification, Sensing mechanism of Mechanical, Electrical, Thermal, Magnetic, Optical, Chemical and Biological Sensors.

Sensor Technology: Concept of clean room, Vacuum systems, Thin Film Materials and processes (Lithography, oxidation, sputtering, diffusion, CVD, micro machining, Wafer bonding, Wire bonding and Packaging.

Sensor Modeling: Numerical modeling techniques, Model equations, different effects on modelling (mechanical, electrical, thermal, magnetic, optical, chemical and biological and example of modelling.

Sensor characterization and Calibration: Basic measurement and characterization systems, study of static and dynamic Characteristics, Sensor reliability, Ageing Test, failure mechanism.

Sensor Applications: Pressure Sensor with embedded electronics, Accelerometer, RF MEMS Switch with electronics, Bio-MEMS, environmental monitoring (Gas Sensors).

Future Aspects of MEMS: NEMS, MOEMS, BIO-MEMS, RF MEMS, OPTICAL MEMS.

Course Learning Outcomes (CLO):
After the completion of this course, the students are able to:
1. Understand various micro fabrication technologies.
2. Acquire knowledge about mems & micro sensors.
3. Use characterization tools.
4. Transfer knowledge to device applications

Recommended Books:
Evaluation Scheme:

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PVL211: VLSI ARCHITECTURE

Course Objective: The motive of this course is to inculcate the knowledge of the different processors; their architecture and organizational intricacies. For performance enhancement consideration is given to various instruction level and memory management techniques such as pipelining, parallelism, instruction scheduling, hierarchical memory management etc. Study of the superscaler architecture organization, design issues and Power PCs is to be carried out.

Introduction: Review of basic computer architecture, quantitative techniques in computer design, measuring and reporting performance. CISC and RISC processors. Processor organization and Architectural Overview.

Pipelining: Basic concepts, instruction and arithmetic pipeline, data hazards, control hazards, and structural hazards, techniques for handling hazards. Exception handling. Pipeline optimization techniques, dynamic instruction scheduling

Hierarchical memory technology: Inclusion, Coherence and locality properties; Cache memory organizations, Techniques for reducing cache misses; Virtual memory organization, mapping and management techniques, memory replacement policies.

Instruction-level parallelism: basic concepts, techniques for increasing ILP, superscalar, super-pipelined and VLIW processor architectures. Array and vector processors.


Course Learning Outcome (CLO):
The students will able to:
1. To review the basics of different processors including architecture and organization
2. To foster ability of handling and designing different types of pipelining techniques; exception handling corresponding instruction scheduling.
3. To understand various memory organization and management techniques
4. To Understand the various advanced architectures.
5. To achieve the understanding of parallel, shared architectures and important organizational details of superscaler architecture

Text Books:

**Reference Books:**

**Evaluation Scheme:**

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</table>
PWC321: NEXT GENERATION WIRELESS SYSTEMS AND NETWORKS

Course Objectives: To understand and gain complete knowledge of wireless systems and networks of next generation like IEEE 802.15, IEEE 802.16, Bluetooth technology, and 4G. To understand the basic concepts of cognitive and software defined radio, Mobile IP, IPv6 versus IPv4, Wireless Application Protocol (WAP), IP on Mobile Ad Hoc Networks.


3G Mobile Cellular Technologies: CDMA2000, WCDMA, TD-SCDMA.


Laboratory Work: Matlab related simulation experiments.

Course Learning Outcomes (CLO): At the end of the course, the students should be able to:

1. Understand the fundamentals and operation of next generation wireless communication systems like 3G, OFDM, MIMO systems, cognitive radio and 3GPP (4G),
2. Understand broadband wireless networks and all IP based wireless networking.
3. Analyze the performance of next generation wireless communication systems.
Recommended Books

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Course Objectives: To provide a comprehensive introduction to error correction coding, including both classical block and trellis based codes and recent developments in iteratively decoded codes such as turbo codes and LDPC codes. To understand the coding using Factor Graph methods and message passing. To understand the methods of designing efficient channel encoder and decoder using various coding schemes like Block codes, Turbo coding, convolution coding, cyclic coding and LDPC coding.

Introduction: Codes and ensembles, MAP and ML decoding, APP processing, Channel Coding Theorem, Linear Codes and complexity, Hamming codes, Gallager’s parity check codes, Decoding complexity of linear codes, Convolutional codes and its complexity, Iterative coding and decoding, Extending, Puncturing and shortening of codes.

Factor Graphs: Distributive law, Graphical representation of factorization, Recursive determination of marginals, Marginalization via message passing, Decoding via message passing, Limitations of cycle-free codes, Message passing on codes with cycles.

Binary Erasure Channel: Channel model, Transmission via linear codes, Tanner graphs, Low density parity check (LDPC) codes, Message passing decoder, Computation graph and tree ensemble, Convergence to tree channel, Density evolution, Monotonicity, Gallager’s lower bound on density, Sparse distribution, Maxwell decoder.

Turbo Codes: Structure and encoding, decoding of turbo codes, Density evolution, Stability condition, Exit charts, MAP performance, High performance turbo codes, Sliding window turbo codes, Turbo coded modulation, Set partitioning, Multi level codes.

Course Learning Outcomes (CLO): At the end of this course, the students should be able to:

1. Understand the basic concepts of different types of coding techniques like block codes, trellis based codes, iteratively decoded codes such as turbo codes and LDPC codes.
2. To efficiently design the channel encoder and decoder using different coding schemes.
3. To evaluate the performance of different channel encoders.

Recommended Books:

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PWC323: WIRELESS BROADBAND NETWORKS

Course Objectives: To understand and gain the complete knowledge of wireless broadband networks which include the concepts of OFDM, OFDMA, MAC and routing protocols for broadband networks, modem design, concepts of equalizer and Convergence of Networks of various of networks.


Wireless Broadband Networking with WiMAX: WiMAX Overview, Competing Technologies, Overview of the Physical Layer, PMP Mode, Mesh Mode, Multihop Relay Mode.

Wireless Local Area Networks: Network Architectures, Physical Layer of IEEE 802.11n, Medium Access Control, Mobility Resource Management; Quality of Service, Applications.

Wireless Personal Area Networks: Network Architecture, Physical Layer, Medium Access Control, Mobility Resource Management, Routing, Quality of Service, Applications.
Convergence of Networks: GPP/WLAN Interworking, IEEE 802.11u Interworking with External Networks, LAN/WLAN/WiMax/3G Interworking Based on IEEE 802.21 Media-Independent Handoff, Future Cellular/WiMax/WLAN/ WPAN Interworking, Analytical Model for Cellular/WLAN Interworking.

Course Learning Outcomes (CLO):
At the end of this course, the students should be able:
1. To understand the concepts and operation of wireless broadband networks.
2. To analyze and evaluate the performance of physical layer (OFDM and OFDMA), data link layer (MAC protocols) and routing protocols for wireless broadband networks.
3. To understand different types of networks and the concepts of convergence of networks.

Recommended Books:

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PEC217: MICROSTRIP ANTENNAS

Course Objective: The objectives of this course is to provide general knowledge of the fundamental principles and concepts related with micro-strip patch antennas and circuits, their analysis, design, fabrication and test are addressed.


Micro-Strip Antenna Arrays: Array theory, Array calculations and analysis, array architectures, corporate array design, Resonant series fed array design, Series fed traveling wave array design.


Slot-Line: Introduction of Slot-lines, Slot-line Analysis, Design Considerations, Slot-line Discontinuities, Slot-line Transitions, Slot-line Applications.

Coplanar Lines and Wave Guides: Introduction of Coplanar Waveguide and Coplanar Strips, Quasi-Static Analysis, Design Considerations Losses, Effect of Tolerances, Comparison with Micro-strip Line and Slot-line, Transitions, Discontinuities in Coplanar Waveguide, Coplanar Line Circuits.


Micro-Strip Circuit Design: impedance transformers, filters, isolators and phase shifters.

Minor Project: To be assigned by concerned instructor/course-coordinator.

Course Learning Outcomes (CLOs):
1. Ability to understand the basic concept of micro-strip antennas, methods of analysis and configurations.
2. Ability to understand micro-strip antennas arrays.
3. To understand the physical significance of discontinuities.
4. To understand the significance of different micro-strip feed mechanism available.
5. Ability to understand coupled micro-strip line with multiband and broadband behavior.
6. To know about CPW feeding technique and its implementation.
Text Books:

Reference Books:

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PEC206: MACHINE LEARNING  

Course Objective: At the end of the course the student should be able to represent multidimensional data in the pattern space and segment the same according to standard paradigms. The student should understand Bayesian decision criteria and probabilistic inferences. The student should understand formation of decision boundaries using a neural network and unsupervised learning paradigms. He should be able to apply the concepts learnt in real world scenarios.

Introduction to Machine Perception: Historical perspective, Pattern recognition systems, Segmentation and Grouping, Feature Extraction, Classification.

Learning and Adaptation Processes: Pattern space and decision boundaries, McCulloch-Pitts model of a neuron, Learning tasks, Hebbian learning, Supervised and unsupervised learning, Batch learning and On-Line learning.


Bayesian Decision Theory: Two category Bayesian classification, Minimum Error Rate classification, Minimax criterion, Neyman-Pearson criterion, Discriminant Functions and Decision Surfaces, Error Probabilities and Integrals, Error bounds viz. Chernoff Bound and Bhattacharya Bound.


Minor Project: To be assigned by concerned instructor/course-coordinator.
Course Learning Outcomes (CLO): 
1. Familiarization with parametric and non parametric estimations. 
2. Ability to visualize data in the pattern space. 
3. Ability train and test classifiers using supervised learning. 
4. Ability to apply clustering algorithms to process big data real time. 
5. Apply Bayesian parameter estimation to real world problems.

Text Books: 
2. Simon Hykin, Neural Networks and Learning Machines, Prentice Hall of India (2010) 

Reference Books: 

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Course Objective: To provide students with the ability to apply adaptive filtering techniques to real-world problems (e.g. adaptive interference cancellation, adaptive equalization) in order to improve the performance over static, fixed filtering techniques. To provide a theoretical basis of adaptive signal processing necessary for the students to extend their area of study to additional applications, and other advanced concepts in statistical signal processing.

Signals and Systems: System theory, stochastic processes Gauss Markov model, Representation of stochastic processes, likelihood and sufficiency, Hypothesis testing, decision criteria, multiple measurements.


System Modeling and Identification: System identification based on FIR (MA), All Pole (AR), Pole Zero (ARMA) system models, Least square linear prediction filter, FIR least squares inverse filter, predictive de convolution, Matrix formulation for least squares estimation: Cholesky decomposition, LDU decomposition, QRD decomposition, Grahm - Schmidt orthogonalization, Givens rotation, Householder reflection, SVD.

Adaptive Filtering: Least square method for tapped-delay line structures, Least Mean Squares (LMS) and Recursive Least Squares (RLS) algorithms and their convergence performance, IIR adaptive filtering and Transform domain adaptive filtering, introduction of different types of LMS, RLS and Kalman filters and their relationship with each other.


Non-Stationary Signal Analysis: Time frequency analysis, Cohen class distribution, Wigner-Ville Distribution, Wavelet Analysis.

Applications: Noise and echo cancellation, Parameters estimation in Radar systems, Dynamic target tracking, Application to pattern classification and system identification, channel identification and equalization, Generalized inverses, regularization of ill-posed problems,
Interpolation and approximation by least squares and minimax error criteria, Optimization techniques for linear and nonlinear problems, Model order selection, MUSIC, ESPRIT algorithms, Signal Analysis with Higher order Spectra, array processing, Beam forming, time-delay estimation, successive and parallel interference cancellers.

**Minor Project:** To be assigned by concerned instructor/course-coordinator

**Course Learning Outcomes (CLOs):**
1. Acquired knowledge about Signals and Systems.
2. Ability to identify Estimation Theory.
3. Foster ability to work using Estimation of Waveforms.
4. Foster ability to work using System Modelling and Identification.
5. Acquired knowledge about Adaptive Filtering.
6. Acquired knowledge about Adaptive Equalization.
7. Acquired knowledge about Non-stationary Signal Analysis and its Applications.

**Text Books:**

**Reference Books:**

**Evaluation Scheme:**

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PEC338: ROBOTICS AND AUTOMATION

Course Objectives: To make students understand principles of sensors and Robotics, interaction with outer world with the help of sensors, image processing for machine learning, object recognition and kinematics for robots and its interaction with objects.

Introduction: Definition and Need for Robots, Robot Anatomy, Co-ordinate Systems, Work Envelope, types and classification, Specifications, Pitch, Yaw, Roll, Joint Notations, Speed of Motion, Pay Load, Robot Parts and Their Functions, Different Applications.


Robot Kinematics & Programming: Forward Kinematics, Inverse Kinematics and Differences; Forward Kinematics and Reverse Kinematics of Manipulators with Two, Three Degrees of Freedom (In 2 Dimensional), Four Degrees of Freedom (in 3 Dimensional), Deviations and Problems Teach Pendant Programming, Lead through programming, Robot programming Languages, VAL Programming, Motion Commands, Sensor Commands, End effecter commands.

Minor Project: To be assigned by concerned instructor/course-coordinator

Course Learning Outcomes (CLO):
1. To understand basics of robotics and their functionality.
2. To understand fundamentals of sensors.
3. To understand various driver systems for robots.
4. To understand image processing and computer vision for robotics.
5. To understand development of algorithms for robot kinematics.
Text Books:

Reference Books:

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PEC321: ADVANCED OPTICAL TECHNOLOGIES

 Course Objective: To understand advanced optical fibre systems like fibres, lasers, modulation techniques, optical amplifiers, MEMS and light wave communication.


 High-Speed Modulation: Introduction, principles and mechanisms of external optical modulation, high-speed modulation, modulators based on phase changes and interference, intensity modulators based on absorption changes, Introduction, basic principle of Optical Injection Locking (OIL), modulation properties of OIL VCSELS, RF link gain enhancement of OIL VCSELS, nonlinearity and dynamic range of OIL VCSELS, Traveling-wave electro-absorption modulators (EAMS), High-efficiency modulators for 100 gb/s and beyond novel types of modulators.

 Optical Amplifiers: Types of optical amplifiers, Er/Yb doped fiber amplifiers, Raman amplifier, Semiconductor optical amplifier, single-mode fiber 980-NM pumps, Materials for 980-nm Pump Diodes, Optical Beam Narrow Stripe Technology, Output Power Scaling, Spectral Stability, Packaging, Failure Rate, High Power Photonics.


 Minor Project: To be assigned by concerned instructor/course-coordinator
Course Learning Outcomes (CLO):
1. Understanding of Fundamentals, advantages and advances in optical devices and circuits.
2. Know-how of advanced optical waveguides, detectors, amplifiers, silicon photonics and MEMS applications in photonics.

Text Books:

Reference Books:

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PVL333: SYSTEM ON CHIP

Course Objectives: In this course the students will learn SOC design processes, ASIC design flow, EDA tools, architecture design and test optimization with system integration issues.

Overview of SOC Design Process: Introduction, Top-down SoC design flow, Metrics of SoC design, Techniques to improve a specific design metric, ASIC Design flow and EDA tools.


SOC Design and Test Optimization: Design methodologies for SoC, Noise and signal integrity analysis, System Integration issues for SoC, SoC Test Scheduling and Test Integration, SoC Test Resource partition.

Course Learning Outcomes (CLO): After the completion of this course, the students are able to:
1. Understand the ASIC Design flow and EDA tools.
2. Acquire knowledge about Top-down soc design flow.
3. Apply knowledge about Front-end and back-end chip design.
4. Model designing communication Networks.
5. Analyse the design space exploration.
6. Interpret the design methodologies for soc

Recommended Books:

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Course Objectives: In this course the students will learn the basic fundamental of OTA architecture, amplifiers, voltage followers, floating gate circuits along with its applications, switched capacitor techniques and implementations.

Review Of Basic Op Amps and OTA Architectures, Conventional Op Amps, OP Amp Limitations, 3 dB time constant computation.

Nested and Reversed Gm-C Op Amplifiers. Recent settling time techniques, Enhanced Gm-C Amp for Large capacitive load.

Line Driver Amplifiers, Band gap and references, Low voltage cells, Low Voltage current source implementation,
Flipped voltage follower and applications, Rail-to-rail amplifiers, Fully balanced fully symmetric circuits.
Bulk-driven circuits, Floating Gate circuits and its applications.
LDO Fundamentals, Class D amplifiers, Multipliers: Power, linearity and area tradeoffs
P-N Rail to Rail Stages and Low Voltage Cells, Voltage References
Common-Mode Feedback & Feedforward: Theory and Practice, Non-linearity issues and Noise Considerations
Linearized OTA and Fully-Differential OTA: CMFB control techniques. Negative resistors and capacitors.
Low Voltage Switched-Capacitor Techniques, Comparators & Sample and Hold circuits
Negative Capacitor and Resistor Implementations

Course Learning Outcomes (CLO):
After the completion of this course, the students are able to:

1. Apply knowledge of mathematics, science, and engineering to design and analysis of modern analog integrated circuits.
2. Emphasize the design of practical amplifiers, small systems and their design parameter trade-offs.
3. Understand the relationships between devices, circuits and systems.
4. Participate and function within multi-disciplinary teams.

Recommended Books:
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Course Objectives: In this course the students will learn redundant number systems, algorithms for fast addition, VLSI implementation aspects, High speed multiplication, VLSI layout considerations, algorithms for fast division and impact of hardware technology.

Numbers and Arithmetic: Review of Number systems, their encoding and basic arithmetic operations, Class of Fixed-Radix Number Systems, Unconventional fixed-point number systems, Representing Signed Numbers, Negative-radix number Systems, Redundant Number Systems, Residue Number Systems.


High-Speed Multiplication: Basic Multiplication Schemes, Shift/add multiplication algorithms, Programmed multiplication, Basic hardware multipliers, Multiplication of signed numbers, Multiplication by constants, Preview of fast multipliers, High-Radix Multipliers, Modified Booth’s recoding, Tree and Array Multipliers, Variations in Multipliers, VLSI layout considerations.

Fast Division and Division Through Multiplication: Basic Division Schemes, Shift/subtract division algorithms, Programmed division, Restoring hardware dividers, Non-restoring and signed division, Division by constants, Preview of fast dividers, High-Radix Dividers, Variations in Dividers, Combined multiply/divide units, Division by Convergence, Hardware implementation.


Course Learning Outcomes (CLO):
After the completion of this course, the students are able to:
1. Understand power fundamentals: design objective, quantification of energy and power.
2. Work with fast adders.
3. Analyze the issues related to trade-off between cost, speed and accuracy.
4. Work with high throughput, low power algorithms.

Recommended Books:

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Course Objectives: To understand and gain the complete knowledge of wireless sensor networks. To understand the fundamentals of various architectures, different layers protocols and their applications, topology Control, routing protocols details, transport layer and quality of service. To understand the concepts of data-centric and content-based networking.

Introduction: The vision of Ambient Intelligence, Application examples, Types of applications, Challenges for WSNs, Why are sensor networks different?, Enabling technologies.

Single Node Architecture: Hardware components, Energy consumption of sensor nodes, Operating systems and execution environments, Some examples of sensor nodes, Conclusion.

Network Architecture: Sensor network scenarios, Optimization goals and figures of merit, Design principles for WSNs, Service interfaces of WSNs, Gateway concepts, Conclusion.

Physical Layer: Introduction, Wireless channel and communication fundamentals, Physical layer and transceiver design considerations in WSNs.

MAC Protocols: Fundamentals of (wireless) MAC protocols, Low duty cycle protocols and wakeup concepts, Contention-based protocols, Schedule-based protocols, The IEEE 802.15.4 MAC protocol, How about IEEE 802.11 and Bluetooth.


Topology Control: Motivation and basic ideas, Flat network topologies, Hierarchical networks by dominating sets, Hierarchical networks by clustering, Combining hierarchical topologies and power control, Adaptive node activity.


Data-Centric and Content-based Networking: Introduction, Data-centric routing, Data aggregation, Data-centric storage.

Transport Layer and Quality of Service: The transport layer and QoS in wireless sensor networks, Coverage and deployment, Reliable data transport, Block delivery, Congestion control and rate control.

Course Learning Outcomes (CLO):
At the end of the course, the students should be able to:
1. Understand the fundamentals of various Architectures, different layers protocols and their applications, topology Control and routing protocols details.
2. Understand the data-centric and content-based networking.
3. Understand the transport layer and quality of service.

Recommended Books:


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PWC336: WIRELESS COMMUNICATION PROTOCOLS

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**Course Objectives:** To understand wireless networking, Mobile IP and wireless access protocols, Introduction to mobile network layers and WAP protocols. Study of wireless ATM and Hyper LAN.

**Introduction To Wireless Networking:** Introduction, Difference between wireless and fixed telephone networks, Development of wireless networks, Traffic routing in wireless networks.

**Mobile IP And Wireless Access Protocol:** Mobile IP Operation of mobile IP, Co-located address, Registration, Tunneling, WAP Architecture, overview, WML scripts, WAP service, WAP session protocol, wireless transaction, Wireless datagram protocol.

**Bluetooth:** Overview, Radio specification, Base band specification, Links manager specification, Logical link control and adaptation protocol. Introduction to WLL Technology.


**Wireless Application Protocol:** WAP(1.0) Introduction, Main Objectives, Integration of WAP components, Stack arrangement with WAP, WAP network, Protocol stack of WAP, WAP client architecture, WAP network architecture, WAP (2.0): Advantages, Main architectural components of WAP 2.0, WAP Programming model, Uses of WAP 2.0 additional services.

**Wireless ATM & Hiper LAN:** Introduction, Wireless ATM, HIPERLAN, Adhoc Networking and WPAN.

**Course Learning Outcomes (CLO):**
At the end of the course the student should be able to:

1. Understand the basic concepts wireless networking and methods to achieve it.
2. Understand various wireless devices and their utility in wireless communication.
3. Understand various protocols used in wireless communication
4. Able to use the protocols for data optimized performance and to achieve QoS.

**Recommended Books:**

1. *Wireless Communication of Networks* - William Stallings PHI
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PWC337: SPREAD SPECTRUM COMMUNICATION

Course Objectives: To understand the basic concept of spread spectrum communication, spreading codes. To understand and gain complete knowledge of radio channel modeling, OFDM, hybrid multiple access technique like OFDMA, MC-CDMA, BLAST architecture.

Introduction: Radio channel characteristics, Channel Modeling, Channel Statics, ISI and ICI, Discrete Multipath Channel Models, Diversity, Multi Carrier Transmission OFDM, Advantages and drawbacks of OFDM, Applications and standards of OFDM, Spread Spectrum Techniques, Multicarrier Spread Spectrum, MC-CDMA, MC-DS-CDMA

Hybrid Multiple Access Schemes: Multi carrier FDMA, OFDMA, OFDMA with code division multiplexing, distributed DFT, localized DFT, multi carrier TDMA, Pseudo random PPM Ultra Wide Band systems, Comparison of Hybrid multiple access schemes, Multi carrier modulation and demodulation, synchronization, channel estimation, channel coding and decoding, signal constellation, mapping demapping and equalization, Adaptive techniques in multi carrier transmission, RF issues

Applications: 3GPP LTE systems, Requirements on LTE, Radio Access Network Architecture, Radio protocol Architecture, Downlink and Uplink Transmission Scheme, WiMax, System Architecture, WiMax Profiles, Hyper Man and 802.16x, Future mobile communication concepts, VSF-OFCDM access schemes, Wireless LAN, interaction channel for DVB-T:DVB-RCT

Additional Techniques for Capacity and Flexibility Enhancement: MIMO, BLAST architecture, Space-time coding, diversity techniques for multi carrier transmission, spatial pre-coding for multi carrier transmission, software defined radio.

Course Learning Outcomes (CLO):
At the end of the course, the students should be able to:
1. Understand the basic concepts of Spread Spectrum Communication and spreading codes.
2. Understand the basic concepts of OFDM and various Hybrid Multiple Access Schemes
3. Realize the basic ideas of the applications related to the spread spectrum communication
4. Understand the concepts of additional techniques for capacity and flexibility enhancement related to the spread spectrum communication.
5. Understand the basic concepts of MIMO and BLAST Architecture

Recommended Books:

Evaluation Scheme:

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PEC301: ARTIFICIAL INTELLIGENCE

Course Objective: To be familiar with the applicability, strengths, and weaknesses of the basic knowledge representation, problem solving, and learning methods in solving particular engineering problems.


Basic Search Strategies: Problem spaces (states, goals and operators), Problem solving by search, Factored representation (factoring state into variables), Uninformed search (breadth-first, depth-first, depth-first with iterative deepening), Heuristics and informed search (hill-climbing, generic best-first, A*), Space and time efficiency of search, Constraint satisfaction (backtracking and local search methods).

Advanced Search: Constructing search trees, Dynamic search space, Combinatorial explosion of search space, Stochastic search: Simulated annealing, Genetic algorithms, Monte-Carlo tree search, Implementation of A* search, Beam search, Minimax Search, Alpha-beta pruning, Expectimax search (MDP-solving) and chance nodes.

Knowledge Representation: Propositional and predicate logic, Resolution in predicate logic, Question answering, Theorem proving, Semantic networks, Frames and scripts, conceptual graphs, conceptual dependencies.

Reasoning under Uncertainty: Review of basic probability, Random variables and probability distributions: Axioms of probability, Probabilistic inference, Bayes’ Rule, Conditional Independence, Knowledge representations using Bayesian Networks, Exact inference and its complexity, Randomized sampling (Monte Carlo) methods (e.g. Gibbs sampling), Markov Networks, Relational probability models, Hidden Markov Models, Decision Theory Preferences and utility functions, Maximizing expected utility.

Agents: Definitions of agents, Agent architectures (e.g., reactive, layered, cognitive), Agent theory, Rationality. Game Theory Decision-theoretic agents, Markov decision processes (MDP), Software agents, Personal assistants, and Information access Collaborative agents, Information-gathering agents, Believable agents (synthetic characters, modelling emotions in agents), Learning agents, Multi-agent systems Collaborating agents, Agent teams, Competitive agents (e.g., auctions, voting), Swarm systems and Biologically inspired models.

Minor Project: To be assigned by concerned instructor/course-coordinator

Course learning outcomes (CLO):
1. To assess the applications of artificial intelligence and categorize various problem domains, uninformed and informed search methods.
2. To explore and understand advanced search techniques and algorithms like minimax for game playing.
3. To understand the importance of probability in knowledge representation for reasoning under uncertainty.
4. To learn to represent knowledge using Bayesian networks and drawing Hidden Markov Models.
5. To learn the architecture for intelligent agents and implement an intelligent agent.

Text Books:

Reference Books:

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Course Objective: To make students understand principles of Biomedical signals, biomedical signals on time-frequency axis and their analysis, interference of various signals, basics of ECG, EEG, compression of biomedical signals, modelling of biomedical signals.


Data Compression: Lossless & Lossy- Heart Rate Variability – Time Domain measures – Heart Rhythm representation - Spectral analysis of heart rate variability - interaction with other physiological signals.


Minor Project: To be assigned by concerned instructor/course-coordinator

Course Learning Outcomes (CLO):
1. To understand basics of various biomedical signals.
2. To understand fundamentals of processes related to biomedical signals.
3. To understand and analyse various parameters related to biomedical signals.
4. To understand data compression and its application in biomedical field.
5. To understand neurological models of ECG, etc.
Text Books:

Reference Books:

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Course Objective: At the end of the course the student should be able to appreciate the benefits of cloud computing and apply cloud paradigms for evolving businesses. He should be familiar with cloud architectural models and resource allocation strategies. The student should comprehensively be exposed to cloud based services.

Introduction: Basics of the emerging cloud computing paradigm, cloud computing history and evolution, cloud enabling technologies, practical applications of cloud computing for various industries, the economics and benefits of cloud computing.

Cloud Computing Architecture: Cloud Architecture model, Types of Clouds: Public Private & Hybrid Clouds, Resource management and scheduling, QoS (Quality of Service) and Resource Allocation, Clustering.


Data Processing in Cloud: Introduction to Map Reduce for Simplified data processing on Large clusters, Design of data applications based on Map Reduce in Apache Hadoop.

Advanced Technologies: Advanced web technologies (AJAX and Mashup), distributed computing models and technologies (Hadoop and MapReduce), Introduction to Open Source clouds like Virtual Computing Lab (Apache VCL), Eucalyptus.

Cloud Issues and Challenges: Cloud computing issues and challenges like Cloud provider Lock-in, Security etc.

Introduction to Python Runtime Environment: The Datastore, Development Workflow.

Minor Project: To be assigned by concerned instructor/course-coordinator

Course Learning Outcomes (CLO):
1. Familiarization with cloud architectures.
2. Knowledge of data processing in cloud.
3. Ability to apply clustering algorithms to process big data real time.
4. Ability to address security issues in cloud environment.
5. Understand the nuances of cloud based services.
Text Books:

Reference Books:

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PEC205: RF CIRCUIT DESIGN

Course Objective: In this course, students will learn the basic principles of RF devices, from device level, relating to the wireless technologies.


Noise: Thermal noise, flicker, noise review, Noise figure Intrinsic MOS noise parameters, Power match versus noise match.

High Frequency Amplifier Design: Bandwidth estimation using open-circuit time constants, Bandwidth estimation using short-circuit time constants, Rise time, delay and bandwidth, Zeros to enhance bandwidth, Shunt-series amplifiers, Tuned amplifiers.

LNA Design: General Considerations, Problem of Input Matching, LNA Topologies, Gain Switching, Band Switching, High-IP2 LNA, Differential LNA, Other Methods of IP2 Improvement, Nonlinearity Calculations, Degenerated CS Stage, Undegenerated CS Stage, Differential and Quasi-Differential Pairs, Degenerated Differential Pair. Large signal performance, design examples & Multiplier based mixers Sub-sampling mixers.


RF Power Amplifiers: Class A, AB, B, C amplifiers, Class D, E, F amplifiers, RF Power amplifier design examples.


Transceiver Architectures: General Considerations, Receiver Architectures, Transmitter Architectures


**Minor Project:** To be assigned by concerned instructor/course-coordinator.

**Course Learning Outcomes (CLO):**
1. Acquired knowledge about Basic Principles in RF Design.
2. Ability to identify Distributed Systems.
3. Foster ability to work using High Frequency Amplifier Design.
4. Foster ability to work using LNA Design.
5. Acquired knowledge about Mixers and RF Power Amplifiers, Design.

**Text Books:**

**Reference Books:**

**Evaluation Scheme:**

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PEC331: IP OVER WDM

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Course Objective: This is a course dealing with the principles and issues arising in the design of optical networks with WDM technology. The student will study the architecture of WDM networks and related protocols. Emphasis is placed on performance, Internetworking, And transition strategies from today's technology to a future all-optical infrastructure.


Terabit Switching and Routing Network Elements: Transparent Terabit Switching and Routing, Opaque Terabit Switching and Routing, Modular Structure and Greater Granularity, Scalability, Multiple Protocol Interfaces, Architectures and Functionalities, Buffering Scheme, Switching Fabric, IP-Based IPI and OPI, IP-Based Electronic Controller, Multiprotocol Label Switching.


Internetworking Optical Internet and Optical Burst Switching: Overview of Optical Burst Switching, QoS Provisioning with OBS, Survivability Issue in OBS Network, IP-over-WDM Control and Signaling, Network Control, Engineering Control Plane, MPIs/GMPLS Control
Plane for Optical Networks, Signaling Protocol, Optical Internetworking and Signaling, Across the Network Boundary, Sample IP-Centric Control Plane for Optical Networks.

**Survivability in IP-over-WDM Networks:** IP-over-WDM Architecture, Survivability Strategies, Survivable Routing Algorithms, Survivability Layer Considerations, Fault Detection and Notification, Signaling Protocol Mechanism, Survivability in Future IP-over-Optical Networks.

**Optical Internetworking Models and Standards Directions:** Intelligent Optical Network, Internetworking Models to Support, Optical Layer Intelligence, Overlay Model, Static Overlay Model, Dynamic Overlay Model, Peer Model, Optical Internetworking and Ethernet Standards, Gigabit Ethernet.

**Minor Project:** To be assigned by concerned instructor/course-coordinator

**Course Learning Outcomes (CLOs):**

1. To acquire knowledge about protocol design concepts, electro-optic and wavelength conversion.
2. To learn about Terabit Switching and Routing Network Elements & Optical Network Engineering.
3. To study and analyze the performance of Traffic Management for IP-over-WDM and Wavelength-Routing Networks.
4. To analyze Internetworking Optical Internet and Optical Burst Switching, Survivability in IP-over-WDM Networks.
5. To understand Optical Internetworking Models and Standards Directions.

**Text Books:**


**Reference Books:**


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Course Objective: To provide students with the ability to apply adaptive filtering techniques to real-world problems (e.g. adaptive interference cancellation, adaptive equalization) in order to improve the performance over static, fixed filtering techniques. To provide a theoretical basis of adaptive signal processing necessary for the students to extend their area of study to additional applications, and other advanced concepts in statistical signal processing.

Introduction to Artificial Neural Networks: Historical Perspective, Overview of biological Neural System, Popular models of a Neuron, Network architectures, Single & multilayer Perceptron models, Their variants and Applications Terminology, Notations and representation of Neural Networks, Types of activation functions.


Genetic Algorithm: An overview of GA, GA operators, GA in optimization, Selection Techniques, Single-point, Multi-point and Uniform cross-over, Mutation Scheduling, Fitness parameters of parents and offspring.

Hybrid Soft Computational Paradigms: Introduction to Neuro-Fuzzy, Neuro-Genetic and Fuzzy Genetic paradigms, Fuzzy system as a pre-processor, GA for synaptic weight optimization in ANN, Design challenges before a hybrid system, Introduction to ant colony and particle swarm optimization algorithms.

Minor Project: To be assigned by concerned instructor/course-coordinator

Course Learning Outcomes (CLOs):
1. Solve Pattern Classification & Function Approximation Problems.
2. Design appropriate ANN model for a given Problem.
3. Apply data pre-processing techniques.
4. Build Fuzzy inference systems from linguistic models.
5. Undertake genetic optimization and ability to create objective functions for a given optimization problem.

**Text Books:**

**Reference Books:**

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