

THAPAR INSTITUTE OF ENGINEERING AND TECHNOLOGY, PATIALA

Department of Electrical and Instrumentation Engineering

ADVERTISEMENT FOR PROJECT POSITIONS (MeitY SPONSORED PROJECT)

Sanction Order No.: 25(2)/2025-ESDA

Project Title: Design and Development of a Drivetrain Controller for Light Commercial Vehicle with AIS Compliance

About the Project: This project is funded by the Ministry of Electronics and Information Technology (MeitY) under the EVSS-02 scheme. It is a product development initiative targeting Technology Readiness Level (TRL-9) and will be executed in close collaboration with an industry partner. Selected candidates will be required to work both at TIET, Patiala and the industry partner's site, depending on project requirements.

Positions Available

Position	No. of Posts	Monthly Emoluments
Embedded Systems / Firmware Engineer	2	₹50,000/-
Project Associate-I	4	₹35,000/-
Intern (UG: 4, PG: 2)	6	₹5,000/- (UG), ₹10,000/- (PG)

Duration: Initially for 12 months (extendable and co-terminus with the project).

Essential Qualifications: Bachelor's / Master's degree in Electrical Engineering, Electronics & Communication Engineering, Instrumentation / Control Systems, Computer Science Engineering, Mechanical Engineering (relevant specialization) with minimum 60% marks or 7.0 CGPA.

Preferred Specializations

- Embedded Systems / Firmware Engineering
- Microcontrollers and Real-Time Systems
- Power Electronics / Motor Drives
- Instrumentation and Communication Systems
- Thermal / Hardware Design Engineering

Desirable Skills

- Experience in microcontrollers, PCB design, and firmware development
- Hands-on experience in electronics hardware development
- Familiarity with motor control systems, EV powertrain, or automotive electronics
- Willingness to work at industry partner location as well as TIET
- Candidates with GATE/NET qualification or relevant research/industry experience will be preferred

Important Information: a) Selected candidates will work closely with the industry partner for TRL-9 level product development. b) Roles may involve on-site deployment at industry location.

Last Date for Application: 05th May, 2026

Application Process: Interested candidates should email their detailed CV with subject line: “Application for MeitY Project Position” to: **mukesh.singh@thapar.edu**

Applications received after the deadline will not be considered. Only shortlisted candidates will be notified via email for interview. No TA/DA will be provided for attending the interview. Selected candidates may be eligible to enroll for Ph.D. at TIET, subject to institute norms.

Project Team

Principal Investigator (PI)

Dr. Mukesh Singh

Department of Electrical and Instrumentation Engineering

Email: **mukesh.singh@thapar.edu**

Co-Principal Investigators (Co-PI)

Dr. Manoj Badoni

Department of Electrical and Instrumentation Engineering

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Dr. Prashant Singh Rana

Department of Computer Science and Engineering

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Note for Applicants

- Personal details (Name, DOB, Address, Contact details)
- Academic qualifications (degree, year, %/CGPA)
- Technical skills and hardware/embedded experience (approx. 200 words)
- Achievements, publications (if any), and research/industry experience
- Details of two referees